

V-P7H55E

DDR3 1067 Qualified Vendors List (QVL)								
	Part No.	Size	SS/DS	Chip Brand	Chip NO.	Timing	Voltage	IM socket support (Optional)
								1 DIMM 2 DIMM 4 DIMM
Crucial	CT12864BA1067.8FF	1024MB	SS	Micron	9GF22D9KPT	7	-	
Crucial	CT25664BA1067.16FF	2048MB	DS	Micron	9HF22D9KPT	7	-	
ELPIDA	EBJ10UE8EDF0-AE-F	1024MB	SS	ELPIDA	J1108EDSE-DJ-F	-	1.35V(low voltage)	
ELPIDA	EBJ51UD8BAFA-AC-E	512MB	SS	Elpida	J5308BASE-AC-E	-	-	
ELPIDA	EBJ51UD8BAFA-AE-E	512MB	SS	Elpida	J5308BASE-AC-E	-	-	
ELPIDA	EBJ11UD8BAFA-AE-E	1024MB	DS	Elpida	J5308BASE-AC-E	-	-	
ELPIDA	EBJ21UE8EDF0-AE-F	2048MB	DS	ELPIDA	J1108EDSE-DJ-F	-	1.35V(low voltage)	
KINGSTON	KVR1066D3N7/1G	1024MB	SS	Kingston	D1288JEKAPGA7U	7	1.5V	
KINGSTON	KVR1066D3N7/2G	2048MB	DS	Kingston	D1288JEKAPGA7U	7	1.5V	
KINGSTON	KVR1066D3N7/4G	4096MB	DS	Hynix	H5TQ2G83AFR	7	1.5V	
Micron	MT8JTF12864AZ-1G1F1	1024MB	SS	Micron	9GF22D9KPT	7	-	
Micron	MT16JTF25664AZ-1G1F1	2048MB	DS	Micron	9HF22D9KPT	7	-	
SAMSUNG	M378B2873EH1-CF8	1024MB	SS	Samsung	SEC 901 HCF8 K4B1G0846E	-	-	
SAMSUNG	M378B5273BH1-CF8	4096MB	DS	SAMSUNG	846 K4B2G0846B-HCF8	-	-	
Elixir	M2Y2G64CB8HC5N-BE	2048MB	DS	Elixir	N2CB1G80CN-BE	-	-	
Elixir	M2Y2G64CBHA9N-BE	2048MB	DS	-	-	7-7-7-20	-	
Elixir	M2Y2G64CBHC9N-BE	2048MB	DS	Elixir	-	-	-	
Kingtiger	2GB DIMM PC3-8500	2048MB	DS	Hynix	H5TQ1G83AFP G7C	-	-	

4 DIMM Slots

- **1 DIMM**: Supports one module inserted in any slot as Single-channel memory configuration
- **2 DIMM**: Supports one pair of modules inserted into either the blue slots or the black slots as one pair of Dual-channel memory configuration
- **4 DIMM**: Supports 4 modules inserted into both the blue and black slots as two pairs of Dual-channel memory configuration

-When installing total memory of 4GB capacity or more, Windows 32-bit operation system may only recognize less than 3GB. Hence, a total installed memory of less than 3GB is recommended.

-It is recommended to install the memory modules from the slots for better overclocking capability.

-The default DIMM frequency depends on its Serial Presence Detect (SPD), which is the standard way of accessing information from a memory module. Under the default state, some memory modules for overclocking may operate at a lower frequency than the vendor-marked value.