



# Test Report

Product Name : VGA Card

Model No : V8460/Deluxe;V8460/TDV  
V8440/Deluxe;V8440/TDV

Applicant	ASUSTeK COMPUTER INC.
Address	4Fl., No. 150, Li-Te Rd., Peitou, Taipei, Taiwan, R.O.C.

Date of Receipt	May 2, 2002
Date of Test	May 22, 2002
Report No.	025L011E

The Test Results relate only to the samples tested.

The test report shall not be reproduced except in full without the written approval of QuieTek Corporation.



# Declaration of Conformity

The following products is herewith confirmed to comply with the requirements set out in the Council Directive on the Approximation of the laws of the Member States relating to Electromagnetic Compatibility Directive (89/336/EEC). The listed standard as below were applied:

The following Equipment:

Product : VGA Card  
Trade Name : ASUS  
Model Number : V8460/Deluxe;V8460/TDV  
V8440/Deluxe;V8440/TDV

This product is herewith confirmed to comply with the requirements set out in the Council Directive on the Approximation of the laws of the Member States relating to Electromagnetic Compatibility Directive (89/336/EEC).For the evaluation regarding EMC, the following standards were applied:

## RFI Emission:

EN 55022:1998 Class B : Product family standard  
EN 61000-3-2:1995 Class D : Limits for harmonic current emission  
Amendment 1:1998  
Amendment 2:1998  
Amendment 14:2000  
EN 61000-3-3:1995 : Limitation of voltage fluctuation and flicker in low-voltage supply system

## Immunity :

EN 55024:1998 Product family standard

The following importer/manufacturer is responsible for this declaration:

Company Name : \_\_\_\_\_  
Company Address : \_\_\_\_\_  
Telephone : \_\_\_\_\_ Facsimile : \_\_\_\_\_

Person is responsible for marking this declaration:

\_\_\_\_\_  
Name (Full Name) Position/ Title  
\_\_\_\_\_



Date

QuieTek Corporation

Legal Signature

EMC/Safety Test Laboratory  
Accredited by DNV, TUV, Nemko and NVLAP

Date: May 22, 2002  
QTK No.: 025L011E



## Statement of Conformity

The certifies that the following designated product

Product : VGA Card  
 Trade Name : ASUS  
 Model Number : V8460/Deluxe;V8460/TDV  
 V8440/Deluxe;V8440/TDV  
 Company Name : ASUSTeK COMPUTER INC.

This product is herewith confirmed to comply with the requirements set out in the Council Directive on the Approximation of the laws of the Member States relating to Electromagnetic Compatibility Directive (89/336/EEC).For the evaluation regarding EMC, the following standards were applied:

### RFI Emission:

EN 55022:1998 Class B : Product family standard  
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 Amendment 1:1998  
 Amendment 2:1998  
 Amendment 14:2000  
 EN 61000-3-3:1995 : Limitation of voltage fluctuation and flicker in low-voltage supply system

### Immunity :

EN 55024:1998 Product family standard



TEST LABORATORY

Gene Chang/ Manager

The verification is based on a single evaluation of one sample of above-mentioned products. It does not imply an assessment of the whole production and does not permit the use of the test lab. Logo.

# Test Report Certification

Test Date : May 22, 2002

Report No. : 025L011E



Accredited by TUV, DNV, Nemko and NIST (NVLAP)

Product Name : VGA Card

Applicant : ASUSTeK COMPUTER INC.

Address : 4Fl., No. 150, Li-Te Rd., Peitou, Taipei, Taiwan, R.O.C.

Manufacturer : ASUSTeK COMPUTER INC.

Model No. : V8460/Deluxe;V8460/TDV  
V8440/Deluxe;V8440/TDV

Rated Voltage : Power by PC

Trade Name : ASUS

Measurement Standard : EN 55022:1998 Class B  
EN 61000-3-2:1995, Amendment 1:1998, Amendment 2:1998  
Amendment 14:2000, EN 61000-3-3:1995, EN 55024:1998

Measurement Procedure : EN 55022:1998, EN 61000-3-2:1995, EN 61000-3-3:1995,  
IEC 61000-4-2:1995, IEC 61000-4-3:1995, IEC 61000-4-4:1995,  
IEC 61000-4-5:1995, IEC 61000-4-6:1996, IEC 61000-4-8:1993,  
IEC 61000-4-11:1994

Test Result : Complied

The Test Results relate only to the samples tested.

The test report shall not be reproduced except in full without the written approval of Quietek Corporation.

Documented By : Cherry Yu  
( Cherry Yu )

Tested By : Miller Lee  
( Miller Lee )

Reviewed By : Wallace Pan  
( Wallace Pan )

Approved By : Gene Chang  
( Gene Chang )



# Test Report Certification

Test Date : May 22, 2002

Report No. : 0251.011E



Accredited by TUV, DNV, Nemko and NIST (NVLAP)

Product Name : VGA Card

Applicant : ASUSTeK COMPUTER INC.

Address : 4Fl., No. 150, Li-Te Rd., Peitou, Taipei, Taiwan, R.O.C.

Manufacturer : ASUSTeK COMPUTER INC.

Model No. : V8460/Deluxe;V8460/TDV  
: V8440/Deluxe;V8440/TDV

Rated Voltage : Power by PC

Trade Name : ASUS

Measurement Standard : AS/NZS 3548: 1995.

Measurement Procedure : AS/NZS 3548: 1995

Test Result : Complied

The Test Results relate only to the samples tested.

The test report shall not be reproduced except in full without the written approval of QuieTek Corporation.

Documented By : Cherry Yu  
( Cherry Yu )

Tested By : Miller Lee  
( Miller Lee )

Reviewed By : [Signature]  
( Wallace Pan )

Approved By : Gene Chang  
( Gene Chang )



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ATTACHMENT 1: EUT TEST PHOTOGRAPHS

ATTACHMENT 2: EUT DETAILED PHOTOGRAPHS

REFERENCE : LABORATORY OF LICENSE

## 1. General Information

### 1.1. EUT Description

Product Name	VGA Card
Trade Name	ASUS
Model No.	V8460/Deluxe;V8460/TDV V8440/Deluxe;V8440/TDV
EUT Voltage	Power by PC

Note:

- The EUT have one D-sub port, one DVI port, one 3D glasses port, one S-video-out port, one AV-video-out port, one S-video-in port, one AV-video-in port.
- Quietek has verified the construction and function in typical operation. All the test modes were carried out with the EUT in normal operation, which was shown in this test report and defined as:

EMI Test	Mode 1: 1920*1440/75Hz, D-sub+DVI
	Mode 2: 1600*1200/85Hz, D-sub+S-video+AV
	Mode 3:1024*768/60Hz, DVI+S-video+AV
	Mode 4:1024*768/60Hz, D-sub+S-video(INPUT)
	Mode 5: 800*600/60Hz, D-sub+AV(INPUT)
EMS Test	Mode 1: 1920*1440/75Hz, D-sub+DVI



## 1.2. Tested System Details

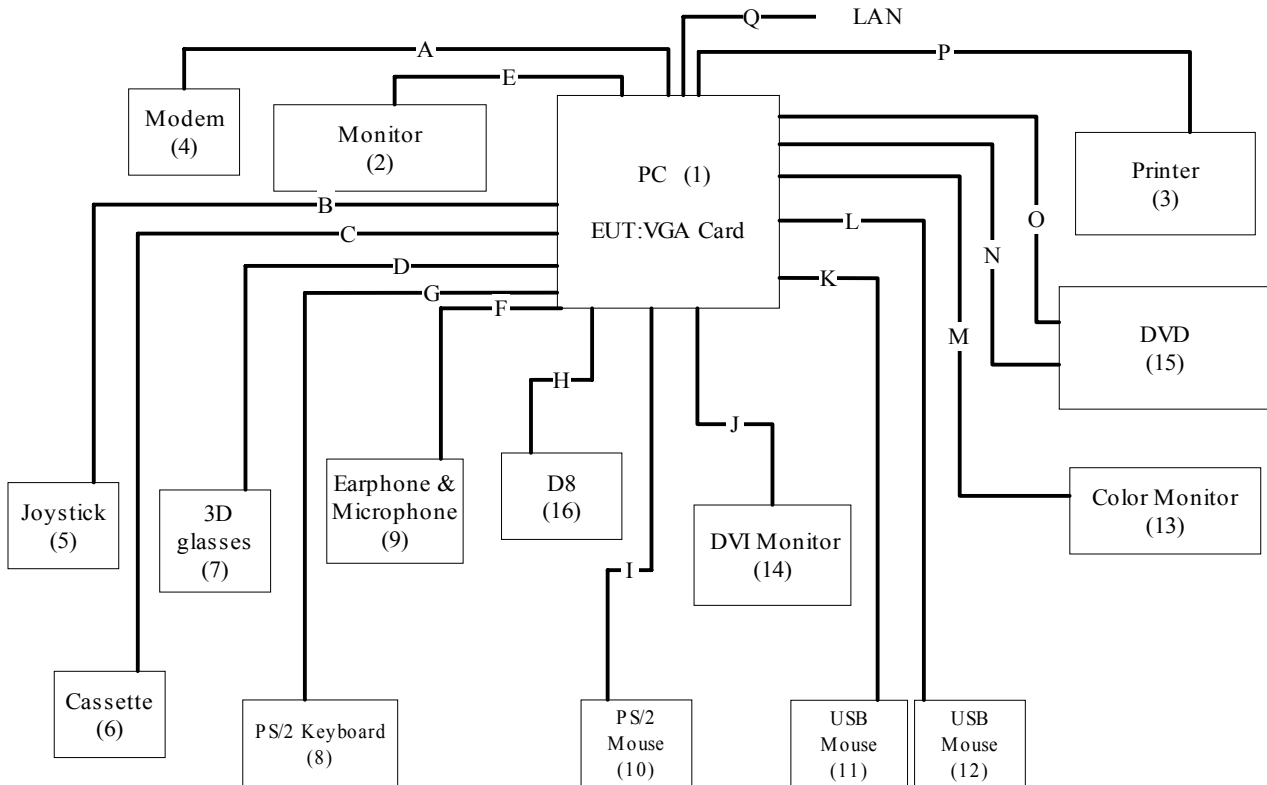
The types for all equipment, plus descriptions of all cables used in the tested system (including inserted cards ) are:

	Product	Manufacturer	Model No.	Serial No.	FCC ID
(1)	PC				N/A
	PC Case	Chenbro	B6251-200	N/A	
	Mother Board	Asus	P4S333-M	N/A	
	CPU	Intel Pentium-4 2.2GHz/100MHz			
	H.D.D.	Quantum	KX20A011	N/A	
	CD-ROM	LITE-ON	LTN-485S	N/A	
	F.D.D.	NEC	FD1231H	N/A	
	VGA Card	EUT	EUT	N/A	
	Sound Card	On Board			
	Lan Card	On Board			
	S.P.S.	Seven team	ST-250BLP	N/A	
(2)	Monitor	SONY	CPD-G500	2737939	FCC DOC
(3)	Printer	EPSON	Color 680	015999	N/A
(4)	Modem	ACEEX	DM-1414	0102027548	IFAXDM1414
(5)	Joystick	GENIUS	MAXFIRE FORCE G-09D	CJ0100200517	FSUGG09
(6)	Cassette	AIWA	HS-TA164	N/A	FCC DC
(7)	3D glasses	EUT	N/A	N/A	N/A
(8)	PS/2 Keyboard	HP	SK-2506	C00083358	FCC DOC
(9)	Microphone& Earphone	TOKTO	SX-MI	N/A	FCC DOC
(10)	PS/2 Mouse	SYNNES	MW3-P	000120549	FCC DOC
(11)	USB Mouse	Logitech	M-BE58	LZE20852001	FCC DOC
(12)	USB Mouse	Logitech	M-BE58	LZE20806612	FCC DOC
(13)	Color Monitor	SONY	PVM-14M2U	105742	FCC DOC
(14)	DVI Monitor	TATUNG	L5TDS	N/A	FCC DOC
(15)	DVD	Mustek	V560	N/A	FCC DOC
(16)	D8	SONY	DCR-TRV5250	1081754	FCC DOC

Note: 1. The power cord of the device 1, 2, 3, 4, 14 and 15 are non-shielded power cord.

Signal Cable Type		Signal cable Description
A.	RS232 cable	Shielded, 1.5m
B.	Joystick cable	Shielded, 1.8m
C.	Audio cable	Non-Shielded, 1.2m
D.	3D glasses cable	Non-Shielded, 1.5m
E.	VGA cable	Shielded, 1.8m with core*2
F.	Earphone & Microphone cable	Non-Shielded, 1.8m
G.	PS/2 Keyboard cable	Shielded, 1.8m
H.	S-video cable	Shielded, 1.5m
I.	PS/2 mouse cable	Shielded, 1m
J.	DVI cable	Shielded, 1.8m
K.	USB mouse cable	Shielded, 1.5m
L.	USB mouse cable	Shielded, 1.5m
M.	AV (RCA) cable	Non-Shielded, 1.2m
N.	S-video cable	Shielded, 1.5m
O.	AV (RCA) cable	Non-Shielded, 1.2m
P.	Printer cable	Shielded, 1.5m
Q.	LAN cable	Non-Shielded, 7m

**1.3. Configuration of tested System**



**1.4. EUT Exercise Software**

1. Setup the EUT and simulators as shown on 1.3.
2. Turn on the power of all equipment.
3. Adjust to appropriate video resolution and run Windows.
4. Run “EMI-TEST”、“Media Player” test program and play Audio.
5. EUT will sends “H” pattern to monitor, the monitor will show “H” pattern on the screen.
6. EUT sends “H” pattern to printer, the printer will print “H” pattern on paper.
7. EUT Connect another simulation PC through LAN port and carry out Read/Write work each other.
8. Repeat the above procedure (3) to (7).

**1.5. Test Facility**

Ambient conditions in the laboratory:

Items	Test Item	Required (IEC 68-1)	Actual
Temperature (°C)	IEC 61000-4-2	15-35	20-35
Humidity (%RH)		30-60	50-65
Barometric pressure (mbar)		860-1060	950-1000
Temperature (°C)	IEC 61000-4-5	15-35	20-35
Humidity (%RH)		10-75	50-65
Barometric pressure (mbar)		860-1060	950-1000
Temperature (°C)	IEC 61000-4-4	15-35	20-35
Humidity (%RH)	IEC 61000-4-8	25-75	50-65
Barometric pressure (mbar)	IEC 61000-4-11	860-1060	950-1000

Site Description:

June 30, 2002 Accreditation on NVLAP  
 NVLAP Lab Code: 200533-0



June 11, 2001 Accreditation on DNV  
 Statement No. : 413-99-LAB11



May 03, 2001 Accreditation on TÜV Rheinland  
 Certificate No.: I9865712-9901



April 24, 2001 Accreditation on Nemko  
 Certificate No.: ELA 165  
 Certificate No.: ELA 162



Site Name: Quietek Corporation  
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 Lin-Kou Shiang, Taipei,  
 Taiwa, R.O.C.  
 TEL : 886-2-8601-3788 / FAX : 886-2-8601-3789  
 E-Mail : [service@quietek.com](mailto:service@quietek.com)

## 2. Conducted Emission

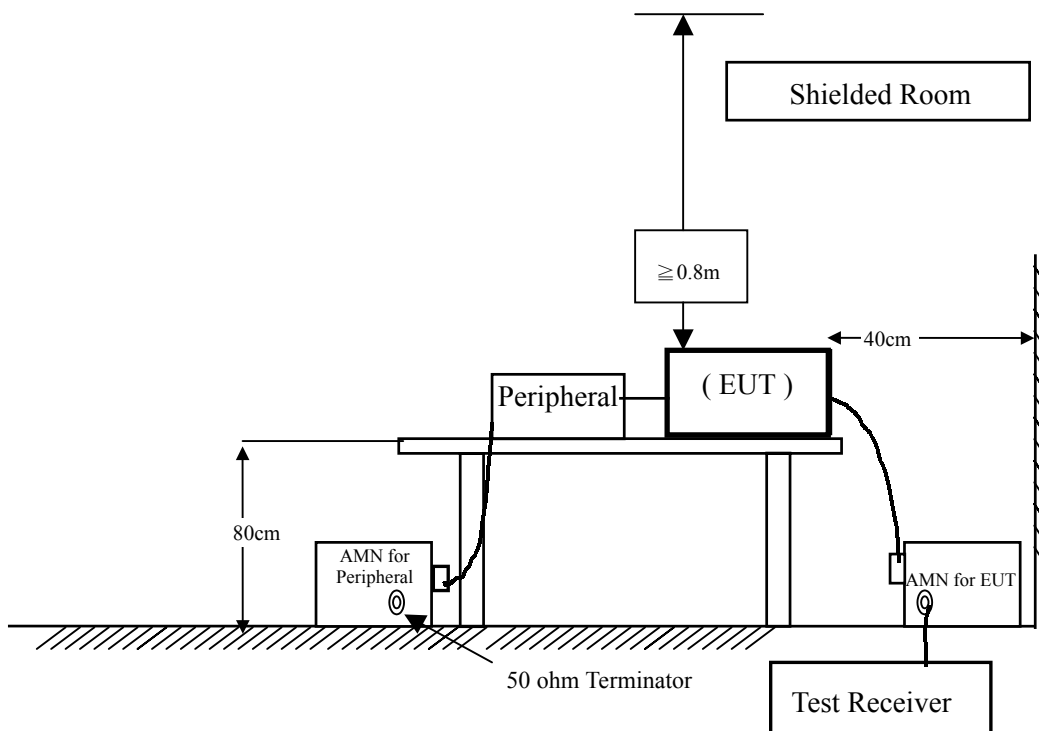
### 2.1. Test Equipment List

The following test equipment are used during the conducted emission test:

Item	Instrument	Manufacturer	Type No./Serial No	Last Cal..	Remark
1	Test Receiver	R & S	ESCS 30/838251/0001	May, 2002	
2	L.I.S.N.	R & S	ESH3-Z5/836679/0023	May, 2002	EUT
3	L.I.S.N.	R & S	ENV 4200/833209/0023	May, 2002	Peripherals
4	Pulse Limiter	R & S	ESH3-Z2	May, 2002	
5	N0.4 Shielded Room			N/A	

Note: All equipment upon which need to calibrated are with calibration period of 1 year.

### 2.2. Test Setup



**2.3. Limits**

<b>EN 55022 Limits (dBuV)</b>				
Frequency MHz	Class A		Class B	
	QP	AV	QP	AV
0.15 - 0.50	79	66	66-56	56-46
0.50-5.0	73	60	56	46
5.0 - 30	73	60	60	50

Remarks : In the above table, the tighter limit applies at the band edges.

**2.4. Test Procedure**

The EUT and simulators are connected to the main power through a line impedance stabilization network (L.I.S.N.). This provides a 50 ohm /50uH coupling impedance for the measuring equipment. The peripheral devices are also connected to the main power through a LISN that provides a 50ohm /50uH coupling impedance with 50ohm termination. (Please refers to the block diagram of the test setup and photographs.)

Both sides of A.C. line are checked for maximum conducted interference. In order to find the maximum emission, the relative positions of equipment and all of the interface cables must be changed according to EN 55022:1998 on conducted measurement.

Conducted emissions were investigated over the frequency range from 0.15MHz to 30MHz using a receiver bandwidth of 9kHz.

**2.5. Test Specification**

According to EN 55022:1998

**2.6. Test Result**

The emission from the EUT was below the specified limits. The worst-case emissions are shown in section 13. The acceptance criterion was met and the EUT passed the test.

### 3. Radiated Emission

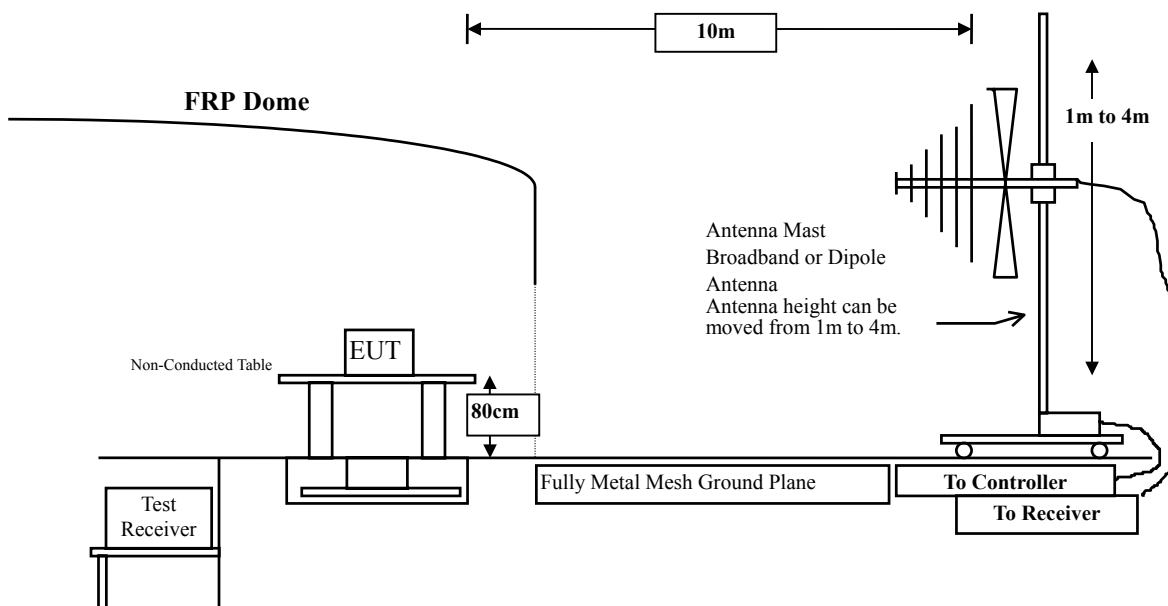
#### 3.1. Test Equipment

The following test equipment are used during the radiated emission test:

Test Site	Equipment	Manufacturer	Model No./Serial No.	Last Cal.
<input type="checkbox"/> Site # 1	Test Receiver	R & S	ESVS 10 / 834468/003	July, 2001
	Spectrum Analyzer	Advantest	R3162/ 00803480	May, 2002
	Pre-Amplifier	Advantest	BB525C/ 3307A01812	May, 2002
	Bilog Antenna	SCHAFFNER	CBL6112B / 2697	Nov., 2001
<input type="checkbox"/> Site # 2	Test Receiver	R & S	ESCS 30 / 836858 / 022	Nov., 2001
	Spectrum Analyzer	Advantest	3162 / 100803466	May, 2002
	Pre-Amplifier	Advantest	BB525C/3307A01814	May, 2002
	Bilog Antenna	SCHAFFNER	CBL6112B / 2705	Oct., 2001
	Horn Antenna	ETS	3115 / 0005-6160	July, 2001
	Pre-Amplifier	QTK	QTK-AMP-01/ 0001	July, 2001
<input checked="" type="checkbox"/> Site # 3	Test Receiver	R & S	ESI 26 / 838786 / 004	May, 2002
	Spectrum Analyzer	Advantest	3162 / 100803480	May, 2002
	Pre-Amplifier	QTK	QTK-AMP-03 / 0003	May, 2002
	Bilog Antenna	SCHAFFNER	CBL6112B / 2697	May, 2002
	Horn Antenna	ETS	3115 / 0005-6160	July, 2001
	Pre-Amplifier	QTK	QTK-AMP-01 / 0001	July, 2001

- Note:
1. All equipments that need to calibrate are with calibration period of 1 year.
  2. Mark "X" test instruments are used to measure the final test results.

#### 3.2. Test Setup



### 3.3. Limits

EN 55022 Limits (dBuV/m)				
Frequency MHz	Class A		Class B	
	Distance (m)	dBuV/m	Distance (m)	dBuV/m
30 – 230	10	40	10	30
230 – 1000	10	47	10	37

### 3.4. Test Procedure

The EUT and its simulators are placed on a turn table which is 0.8 meter above ground. The turn table can rotate 360 degrees to determine the position of the maximum emission level. The EUT was positioned such that the distance from antenna to the EUT was 10 meters.

The antenna can move up and down between 1 meter and 4 meters to find out the maximum emission level.

Both horizontal and vertical polarization of the antenna are set on measurement. In order to find the maximum emission, all of the interface cables must be manipulated according to EN55022:1998 on radiated measurement.

Radiated emissions were investigated over the frequency range from 30MHz to 1GHz using a receiver bandwidth of 120kHz. Radiated was performed at an antenna to EUT distance of 10 meters.

### 3.5. Test Specification

According to EN 55022:1998

### 3.6. Test Result

The emission from the EUT was below the specified limits. The worst-case emissions are shown in section 13. The acceptance criterion was met and the EUT passed the test.

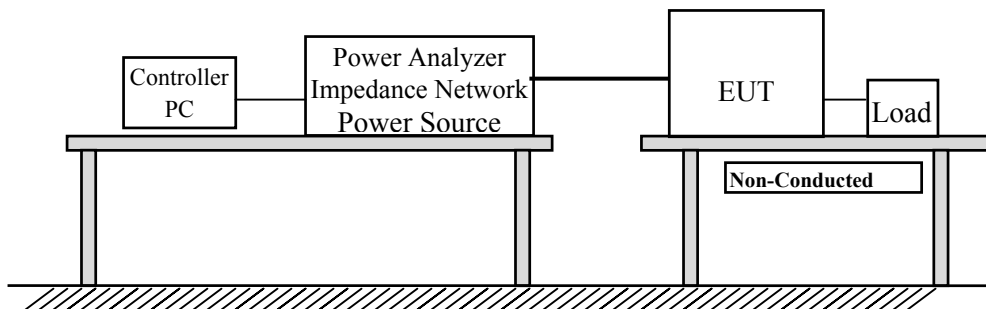
#### 4. Power Harmonics and Voltage Fluctuation

##### 4.1. Power Harmonics and Voltage Fluctuation Test Equipment List

Item	Instrument	Manufacturer	Type No/Serial No.	Last Calibration
1	Power Harmonics Tester	SCHAFFNER	Proflin 2105-400 S/N: HK54148	Jun., 2001
2	Analyzer	SCHAFFNER	CCN 1000-1/X71887	Jun., 2001
3	No.3 Shielded Room			N/A

Note: All equipment upon which need to calibrated are with calibration period of 1 year.

##### 4.2. Test Setup



##### 4.3. Limits

➤Limits of Harmonics Currents

Harmonics Order	Maximum Permissible harmonic current (in amperes)	Harmonics Order	Maximum Permissible harmonic current (in amperes)
Odd harmonics		Even harmonics	
3	2.30	2	1.08
5	1.14	4	0.43
7	0.77	6	0.30
9	0.40	8 ≤ n ≤ 40	0.23 * 8/n
11	0.33		
13	0.21		
15 ≤ n ≤ 39	0.15 * 15/n		



#### **4.4. Test Procedure**

The EUT is supplied in series with power analyzer from a power source having the same normal voltage and frequency as the rated supply voltage and the equipment under test. And the rated voltage at the supply voltage of EUT of 0.94 times and 1.06 times shall be performed.

#### **4.5. Test Specification**

According to EN 61000-3-2:1995, , Amendment 1:1998, Amendment 2:1998, Amendment 14:2000 and EN 61000-3-3:1995

#### **4.6. Test Result**

The measurement of the power harmonics, which test at the extremes of EUT's supply range, was investigated and test result was shown in section 13. The acceptance criterion was met and the EUT passed the test.

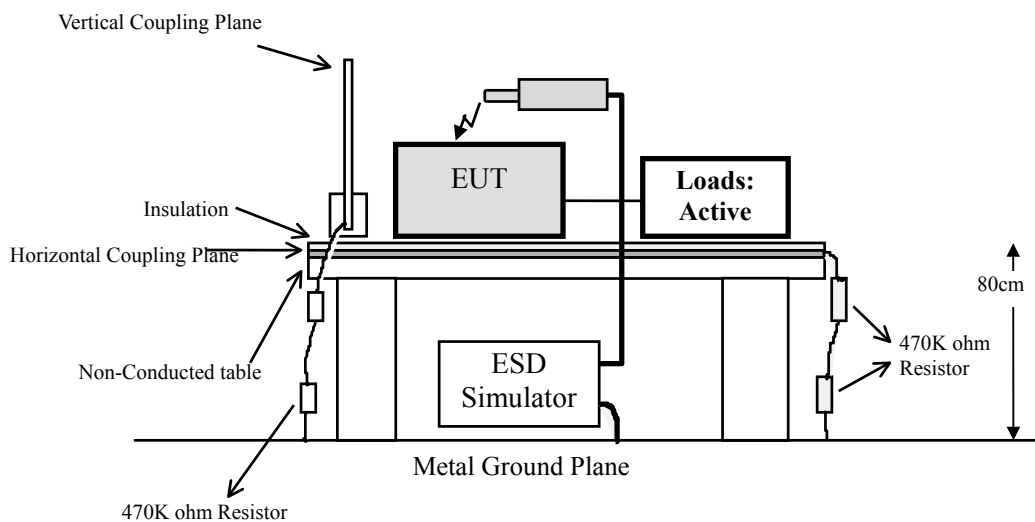
## 5. Electrostatic Discharge (ESD)

### 5.1. Test Equipment

Item	Instrument	Manufacturer	Type No/Serial No.	Last Calibration
1	ESD Simulator System	KeyTek	MZ-15/EC S/N:0112372	Jun., 2001
2	Horizontal Coupling Plane(HCP)	Quietek	HCP AL50	N/A
3	Vertical Coupling Plane(VCP)	Quietek	VCP AL50	N/A
4	No.3 Shielded Room			N/A

Note: All equipment upon which need to calibrated are with calibration period of 1 year.

### 5.2. Test Setup



### 5.3. Test Level

Item	Environmental Phenomena	Units	Test Specification	Performance Criteria
<b>Enclosure Port</b>				
	Electrostatic Discharge	kV(Charge Voltage)	± 8 Air Discharge ± 4 Contact Discharge	B

Remark:

The Contact discharges were applied – at least total 200 discharges at a minimum of four test points.

#### 5.4. Test Procedure

Direct application of discharges to the EUT:

Contact discharge was applied only to conductive surfaces of the EUT.

Air discharges were applied only to non-conductive surfaces of the EUT.

During the test, it was performed with single discharges. For the single discharge time between successive single discharges will be keep longer 1 second. It was at least ten single discharges with positive and negative at the same selected point.

The selected point, which was performed with electrostatic discharge, was marked on the red label of the EUT.

Indirect application of discharges to the EUT:

Vertical Coupling Plane (VCP):

The coupling plane, of dimensions 0.5m x 0.5m, is placed parallel to, and positioned at a distance 0.1m from, the EUT, with the Discharge Electrode touching the coupling plane.

The four faces of the EUT will be performed with electrostatic discharge. It was at least ten single discharges with positive and negative at the same selected point.

Horizontal Coupling Plane (HCP):

The coupling plane is placed under to the EUT. The generator shall be positioned vertically at a distance of 0.1m from the EUT, with the Discharge Electrode touching the coupling plane.

The four faces of the EUT will be performed with electrostatic discharge. It was at least ten single discharges with positive and negative at the same selected point.

#### 5.5. Test Specification

According to IEC 61000-4-2:1995

#### 5.6. Test Result

The measurement of the electrostatic discharge was investigated and test result was shown in section 13. The acceptance criterion was met and the EUT passed the test.

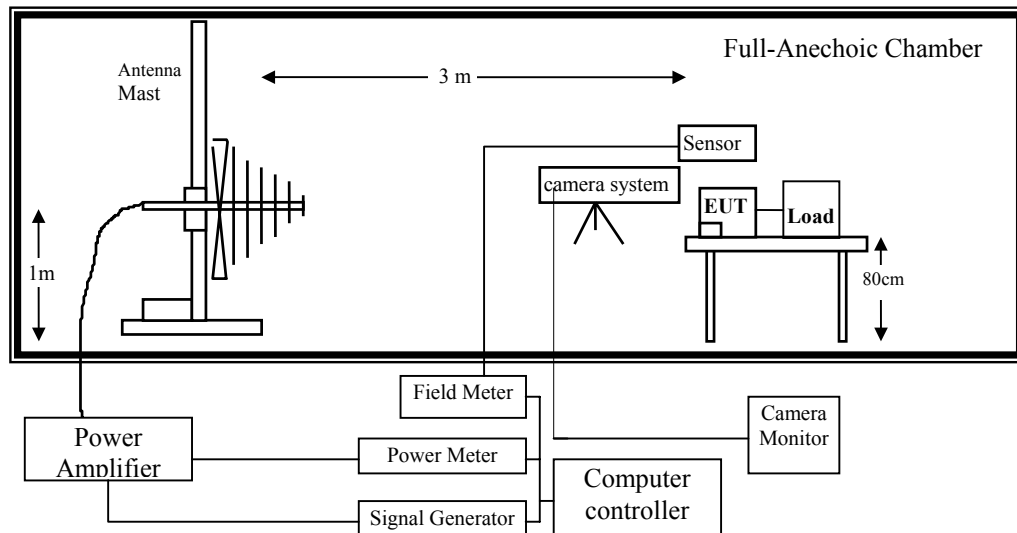
## 6. Radiated Susceptibility (RS)

### 6.1. Test Equipment

Item	Instrument	Manufacturer	Type No/Serial No.	Last Calibration
1	Signal Generator	IFR	2023B / 202302/581	May, 2002
2	Power Amplifier	A & R	500A100AM3 /29369	Aug., 2001
3	Power Amplifier	SCHAFFNER	CBA9413B / 0006	June, 2001
4	Field Strength Sensor	SCHAFFNER	EMC 20 / Y-0028/ Z-0003	June, 2001
5	Power Antenna	SCHWARZBECK	VULB 9166 / 1073	Sep., 2001
6	Power Meter	BOONTON	4232A / 42201	May, 2002
7	No.2 EMC Fully Chamber			July, 2001

Note: All equipment upon which need to calibrated are with calibration period of 1 year.

### 6.2. Test Setup



### 6.3. Test Level

Item	Environmental Phenomena	Units	Test Specification	Performance Criteria
<b>Enclosure Port</b>				
	Radio-Frequency	MHz	80-1000	
	Electromagnetic Field	V/m(Un-modulated, rms)	3	A
	Amplitude Modulated	% AM (1kHz)	80	

#### 6.4. Test Procedure

The EUT and load, which are placed on a table that is 0.8 meter above ground, are placed with one coincident with the calibration plane such that the distance from antenna to the EUT was 3 meters.

Both horizontal and vertical polarization of the antenna and four sides of the EUT are set on measurement.

In order to judge the EUT performance, a CCD camera is used to monitor EUT screen.

All the scanning conditions are as follows:

Condition of Test	Remarks
EN 55024:1998	
1. Field Strength	3 V/M Level 2
2. Radiated Signal	AM 80% Modulated with 1kHz
3. Scanning Frequency	80MHz - 1000MHz
4 Dwell Time	3 Seconds
5. Frequency step size $\Delta f$ :	1%
6. The rate of Swept of Frequency	$1.5 \times 10^{-3}$ decades/s

#### 6.5. Test Specification

According to IEC 61000-4-3:1995

#### 6.6. Test Result

The measurement of the radiated susceptibility was investigated and test result was shown in section 13. The acceptance criterion was met and the EUT passed the test.

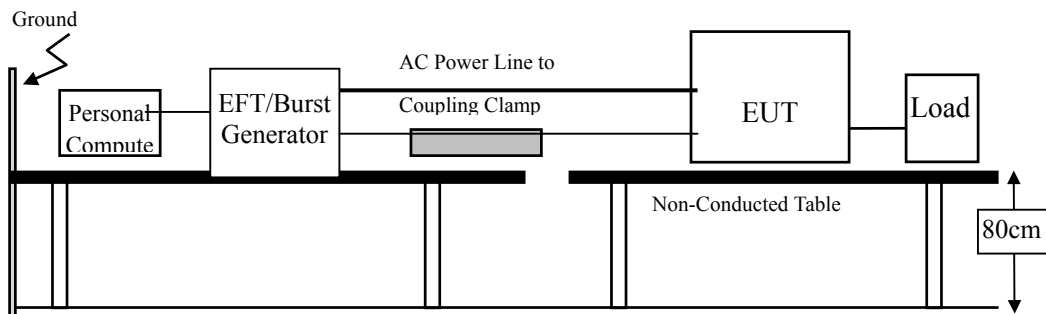
**7. Electrical Fast Transient/Burst (EFT/B)**

**7.1. Test Equipment**

Item	Instrument	Manufacturer	Type No/Serial No.	Last Calibration
1	Fast Transient/Burst Generator	SCHAFFNER	BEST S/N: 300035-008SC	Jun., 2001
2	No.3 Shielded Room			N/A

Note: All equipment upon which need to calibrated are with calibration period of 1 year.

**7.2. Test Setup**



**7.3. Test Level**

Item	Environmental Phenomena	Units	Test Specification	Performance Criteria
<b>Signal Ports and Telecommunication Ports</b>				
	Fast Transients Common Mode	kV (Peak) Tr/Ts ns Rep. Frequency kHz	0.5 5/50 5	B
<b>Input DC Power Ports</b>				
	Fast Transients Common Mode	kV (Peak) Tr/Ts ns Rep. Frequency kHz	0.5 5/50 5	B
<b>Input AC Power Ports</b>				
	Fast Transients Common Mode	kV (Peak) Tr/Ts ns Rep. Frequency kHz	1 5/50 5	B

#### **7.4. Test Procedure**

The EUT and load are placed on a table that is 0.8 meter above a metal ground plane measured 1m\*1m min. and 0.65mm thick min. And projected beyond the EUT by at least 0.1m on all sides.

For Signal Ports and Telecommunication Ports:

The EFT interference signal is through a coupling clamp device couples to the signal and control lines of the EUT with burst noise for 1min.

For Input DC and AC Power Ports:

The EUT is connected to the power mains through a coupling device that directly couples the EFT interference signal.

Each of the Line and Neutral conductors is impressed with burst noise for 1 min.

The length of power cord between the coupling device and the EUT shall be 1m.

#### **7.5. Test Specification**

According to IEC 61000-4-4:1995

#### **7.6. Test Result**

The measurement of the Electrical Fast Transient/Burst was investigated and test result was shown in section 13. The acceptance criterion was met and the EUT passed the test.

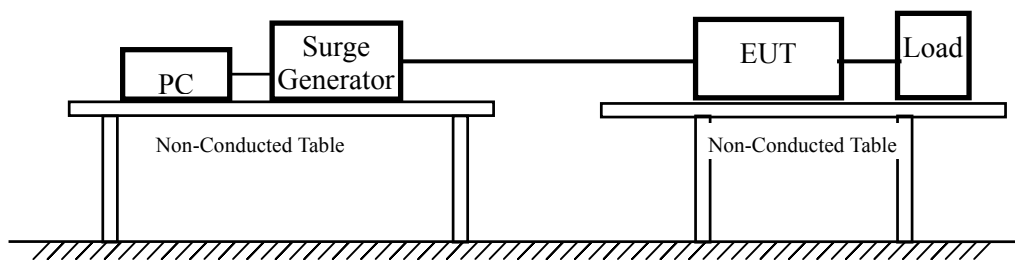
**8. Surge**

**8.1. Test Equipment**

Item	Instrument	Manufacturer	Type No/Serial No.	Last Calibration
1	Surge Generator	SCHAFFNER	BEST S/N: 300035-008SC	Jun., 2001
2	No.3 Shielded Room			N/A

Note: All equipment upon which need to calibrated are with calibration period of 1 year.

**8.2. Test Setup**



**8.3. Test Level**

Item	Environmental Phenomena	Units	Test Specification	Performance Criteria
<b>Signal Ports and Telecommunication Ports</b>				
	Surges	Tr/Ts uS	1.2/50 (8/20)	
	Line to Ground	KV	± 1	B
<b>Input DC Power Ports</b>				
	Surges	Tr/Ts uS	1.2/50 (8/20)	
	Line to Ground	kV	± 0.5	B
<b>AC Input and AC Output Power Ports</b>				
	Surges	Tr/Ts uS	1.2/50 (8/20)	
	Line to Line	kV	± 1	B
	Line to Ground	kV	± 2	

Notes:

- 1) Applicable only to ports which according to the manufacturer’s may directly to outdoor cables.
- 2) Where normal functioning cannot be achieved because of the impact of the CDN on the EUT, no test shall be required.



#### **8.4. Test Procedure**

The EUT and its load are placed on a table that is 0.8 meter above a metal ground plane measured 1m\*1m min. and 0.65mm thick min. And projected beyond the EUT by at least 0.1m on all sides. The length of power cord between the coupling device and the EUT shall be 2m or less.

For Input and Output AC Power or DC Input and DC Output Power Ports:

The EUT is connected to the power mains through a coupling device that directly couples the Surge interference signal.

The surge noise shall be applied synchronized to the voltage phase at  $0^{\circ}$ ,  $90^{\circ}$ ,  $180^{\circ}$ ,  $270^{\circ}$  and the peak value of the a.c. voltage wave. (Positive and negative)

Each of Line-Earth and Line-Line is impressed with a sequence of five surge voltages with interval of 1 min.

#### **8.5. Test Specification**

According to IEC 61000-4-5:1995

#### **8.6. Test Result**

The measurement of the Surge was investigated and test result was shown in section 13. The acceptance criterion was met and the EUT passed the test.

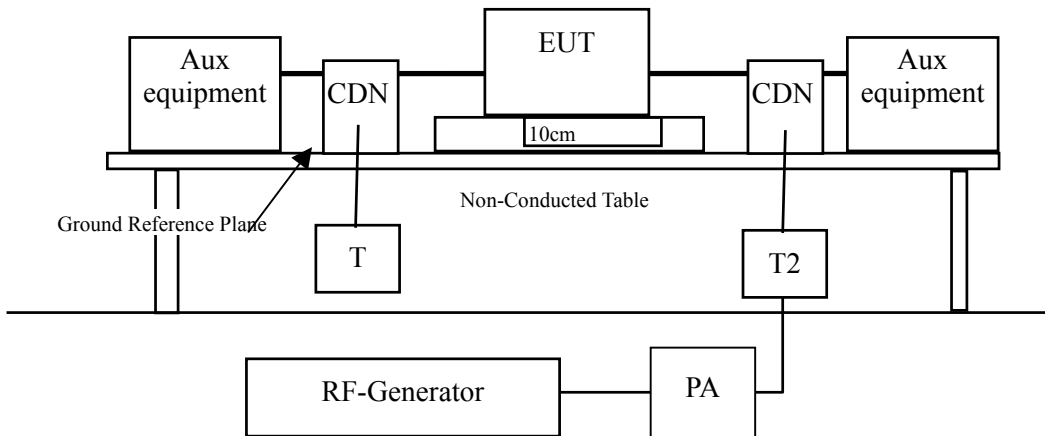
**9. Conducted Susceptibility (CS)**

**9.1. Test Equipment**

Item	Instrument	Manufacturer	Type No/Serial No.	Last Calibration
1	Signal Generator	IFR	2023B / 202302/581	May, 2002
2	Power Amplifier	A & R	500A100AM3 /29369	Aug., 2002
3	Power Amplifier	SCHAFFNER	CBA9413B / 0006	Jun., 2001
4	CDN 1	Schwarzbeck	L801 M2/3 / 1549	Jun., 2001
5	CDN 2	Schwarzbeck	L801 S1 / 1574	Jun., 2001
6	CDN 3	Schwarzbeck	L801 AF4 / 1064	Jun., 2001
7	CDN 4	Schwarzbeck	L801 AF8 / 1070	Jun., 2001
8	CDN 5	FCC	FCC-801-S9 / 9837	Jun., 2001
9	CDN 6	FCC	FCC-801-S15 / 9838	Jun., 2001
10	CDN 7	FCC	FCC-801-S25 / 9839	Jun., 2001
11	50 ohm Terminator	RES-NET	RCX6BM	Jun., 2001
12	6dB Attenuator	BIRD	RFA250NFF10	Jun., 2001
13	EM Clamp	Schwarzbeck	KEMZ 801 / 15928	Jun., 2001
14	No.2 EMC Fully Chamber	N/A		

Note: All equipment upon which need to calibrated are with calibration period of 1 year.

**9.2. Test Setup**



### 9.3. Test Level

Item	Environmental Phenomena	Units	Test Specification	Performance Criteria
<b>Signal Ports and Telecommunication Ports</b>				
	Radio-Frequency	MHz	0.15-80	
	Continuous Conducted	V (rms, Un-modulated)	3	A
		% AM (1kHz)	80	
<b>Input DC Power Ports</b>				
	Radio-Frequency	MHz	0.15-80	
	Continuous Conducted	V (rms, Un-modulated)	3	A
		% AM (1kHz)	80	
<b>Input AC Power Ports</b>				
	Radio-Frequency	MHz	0.15-80	
	Continuous Conducted	V (rms, Un-modulated)	3	A
		% AM (1kHz)	80	

### 9.4. Test Procedure

The EUT are placed on a table that is 0.8 meter height, and a Ground reference plane on the table, EUT are placed upon table and use a 10cm insulation between the EUT and Ground reference plane.

For Signal Ports and Telecommunication Ports

The disturbance signal is through a coupling and decoupling networks (CDN) or EM-clamp device couples to the signal and Telecommunication lines of the EUT.

For Input DC and AC Power Ports

The EUT is connected to the power mains through a coupling and decoupling networks for power supply lines. And directly couples the disturbances signal into EUT.

Used CDN-M2 for two wires or CDN-M3 for three wires.

All the scanning conditions are as follows:

Condition of Test	Remarks
1. Field Strength	130dBuV(3V) Level 2
2. Radiated Signal	AM 80% Modulated with 1kHz
3. Scanning Frequency	0.15MHz – 80MHz
4 Dwell Time	3 Seconds
5. Frequency step size $\Delta f$ :	1%
6. The rate of Swept of Frequency	$1.5 \times 10^{-3}$ decades/s

### 9.5. Test Specification

According to IEC 61000-4-6:1996

### 9.6. Test Result

The measurement of the Conducted Susceptibility was investigated and test result was shown in section 13. The acceptance criterion was met and the EUT passed the test.

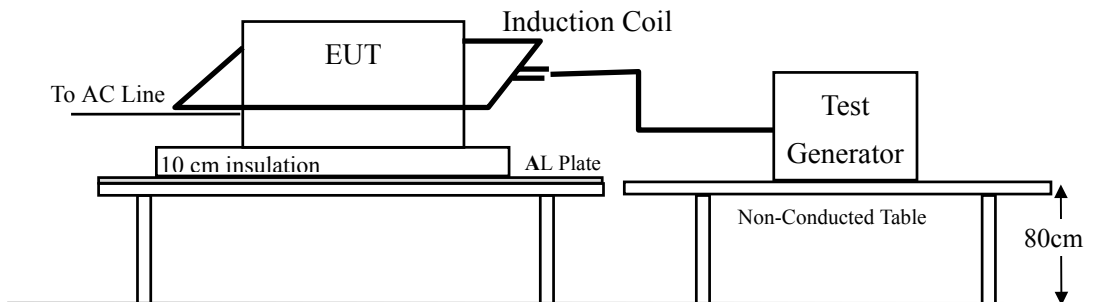
**10. Power Frequency Magnetic Field**

**10.1. Test Equipment**

Item	Instrument	Manufacturer	Type No/Serial No.	Last Calibration
1	Power Line Maganetics	SCHAFFNER	PLINE 1610 S/N: 080 938-05	Jun., 2001
2	Gauss Meter	F.W.BELL	4090	Jun., 2001
3	Magnetic Field Coil	SCHAFFNER	INA702 S/N: 199749-020 IN	Jun., 2001
4	No.3 Shielded Room			N/A

Note: All equipment upon which need to calibrated are with calibration period of 1 year.

**10.2. Test Setup**



**10.3. Test Level**

Item	Environmental Phenomena	Units	Test Specification	Performance Criteria
Enclosure Port				
	Power-Frequency	50	Hz	
	Magnetic Field	1	A/m (r.m.s.)	A

#### **10.4. Test Procedure**

The EUT and its load are placed on a table which is 0.8 meter above a metal ground plane measured at least 1m\*1m min. The test magnetic field shall be placed at central of the induction coil.

The test magnetic Field shall be applied 10minutes by the immersion method to the EUT. And the induction coil shall be rotated by 90° in order to expose the EUT to the test field with different orientation (X, Y, Z Orientations).

#### **10.5. Test Specification**

According to IEC 61000-4-8:1993

#### **10.6. Test Result**

The measurement of the Power Frequency Magnetic Field was investigated and test result was shown in section 13. The acceptance criterion was met and the EUT passed the test.

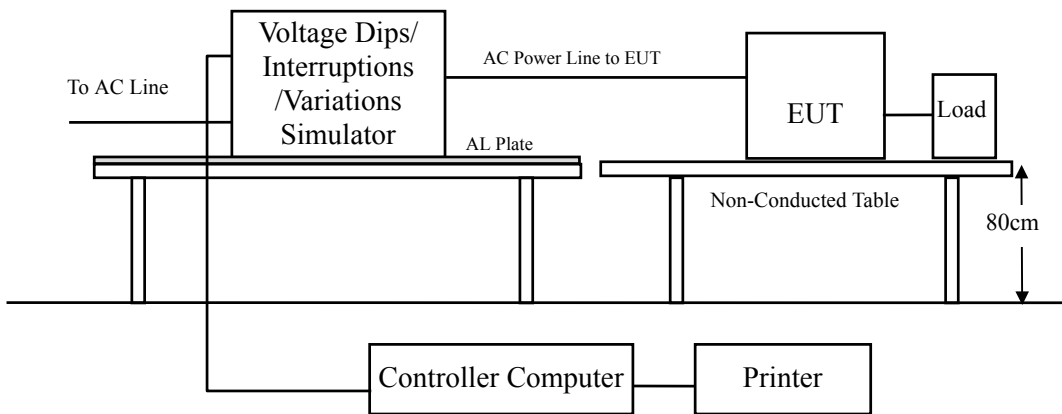
## 11. Voltage Dips and Interruption Measurement

### 11.1. Test Equipment

Item	Instrument	Manufacturer	Type No/Serial No.	Last Calibration
1	Voltage Dips Generator	SCHAFFNER	BEST S/N:300035-008SC	Jun., 2001
2	No.3 Shielded Room			N/A

Note: All equipment upon which need to calibrated are with calibration period of 1 year.

### 11.2. Test Setup



### 11.3. Test Level

Item	Environmental Phenomena	Units	Test Specification	Performance Criteria
Input AC Power Ports				
Voltage Dips		>95	% Reduction	B
		0.5	Period	
		30	% Reduction	C
	25	Periods		
Voltage Interruptions		> 95	% Reduction	C
		250	Periods	

#### **11.4. Test Procedure**

The EUT and its load are placed on a table which is 0.8 meter above a metal ground plane measured 1m\*1m min. And 0.65mm thick min. And projected beyond the EUT by at least 0.1m on all sides. The power cord shall be used the shortest power cord as specified by the manufacturer.

For Voltage Dips/ Interruptions test:

The selection of test voltage is based on the rated power range. If the operation range is large than 20% of lower power range, both end of specified voltage shall be tested. Otherwise, the typical voltage specification is selected as test voltage.

The EUT is connected to the power mains through a coupling device that directly couples to the Voltage Dips and Interruption Generator.

The EUT shall be tested for 30% voltage dip of supplied voltage and duration 500ms, for 95% voltage dip of supplied voltage and duration 10ms with a sequence of three voltage dips with intervals of 10 seconds, and for 95% voltage interruption of supplied voltage and duration 5000ms with a sequence of three voltage interruptions with intervals of 10 seconds. Voltage phase shifting are shall occur at  $0^{\circ}$ ,  $45^{\circ}$ ,  $90^{\circ}$ ,  $135^{\circ}$ ,  $180^{\circ}$ ,  $225^{\circ}$ ,  $270^{\circ}$ ,  $315^{\circ}$  of the voltage.

#### **11.5. Test Specification**

According to IEC 61000-4-11:1994

#### **11.6. Test Result**

The measurement of the Voltage Dips and Interruption was investigated and test result was shown in section 13. The acceptance criterion was met and the EUT passed the test.

## 12. EMC Reduction Method During Compliance Testing

No modification was made during testing.



### 13. Test Result

The test results in the emission and the immunity were performed according to the requirements of measurement standard and process. Quietek Corporation is assumed full responsibility for the accuracy and completeness of these measurements. The test data of the emission is listed as below.

All the tests were carried out with the EUT in normal operation, which was defined as:

EMI Test	Mode 1: 1920*1440/75Hz, D-sub+DVI
	Mode 2: 1600*1200/85Hz, D-sub+S-video+AV
	Mode 3:1024*768/60Hz, DVI+S-video+AV
	Mode 4:1024*768/60Hz, D-sub+S-video(INPUT)
	Mode 5: 800*600/60Hz, D-sub+AV(INPUT)
EMS Test	Mode 1: 1920*1440/75Hz, D-sub+DVI

Note :

- No Deviation from standard procedure
- Deviations from standard procedure

### 13.1. Test Data of Conducted Emission

Date of Test	May 22, 2002	Test Room	No.4 Shielded Room
Test Mode	Mode 1: 1920*1440/75Hz, D-sub+DVI	Product	VGA Card
Test Condition	Line1 & Line2	Test Range	0.15MHz – 30MHz

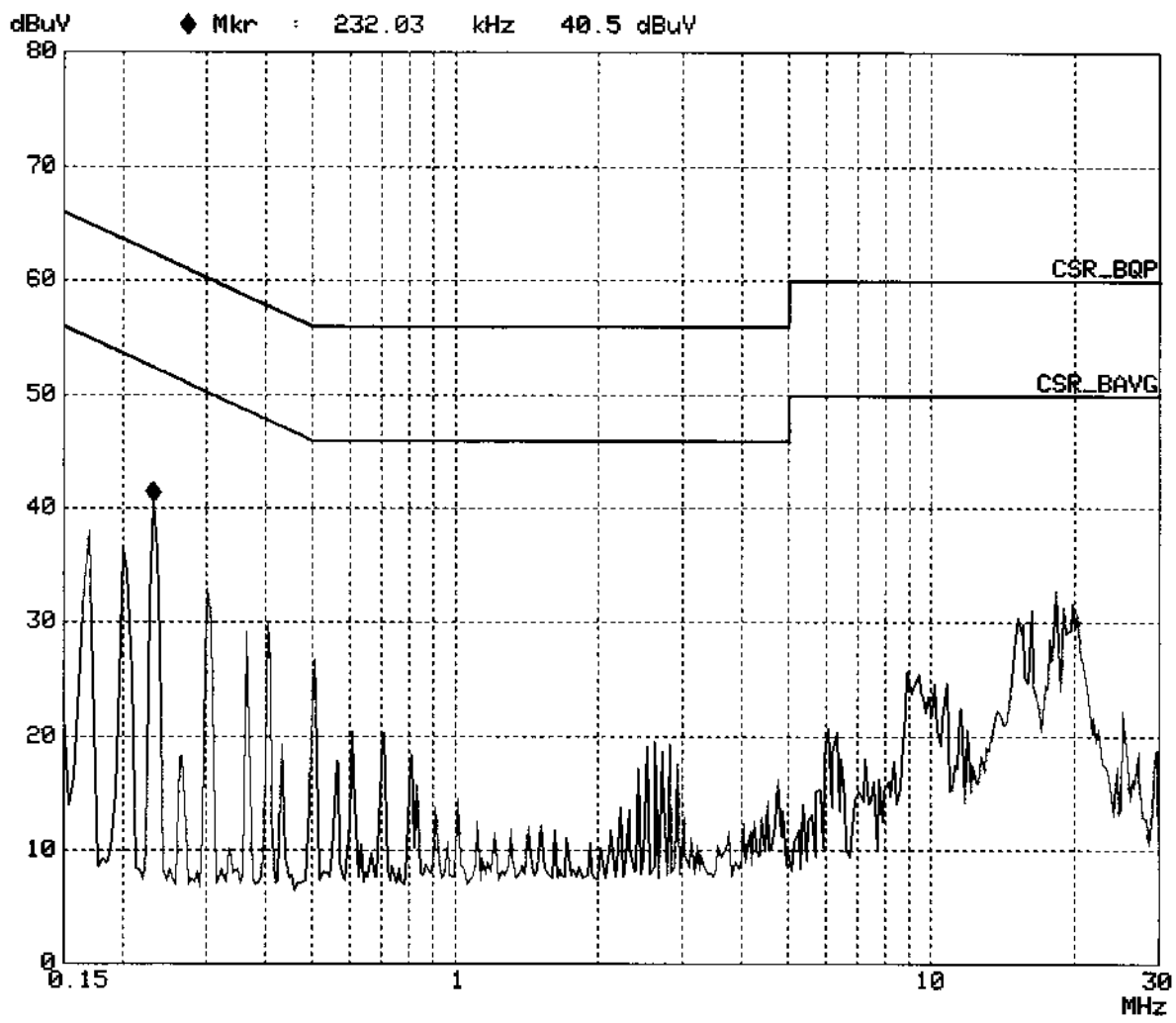
Frequency MHz	Measurement Level (dBuV)				Limits (dBuV)	
	Line1 QP	Line1 AV	Line2 QP	Line2 AV	QP	AV
0.166	--	--	50.75	--	65.18	55.18
0.170	38.13	--	--	--	64.98	54.98
0.201	36.55	--	--	--	63.58	53.58
0.201	--	--	42.29	--	63.58	53.58
0.232	40.19	--	--	--	62.38	52.38
0.301	--	--	40.49	--	60.22	50.22
0.302	33.19	--	--	--	60.18	50.18
0.404	29.83	--	--	--	57.77	47.77
0.501	--	--	27.36	--	56.00	46.00
10.001	--	--	28.93	--	60.00	50.00
18.240	--	--	33.20	--	60.00	50.00
18.244	34.70	--	--	--	60.00	50.00

Note:

1. All Reading Levels are Quasi-Peak and average value.
2. Measurement Level = Reading Level + LISN Factor + Cable loss.
3. "--", means the average measurement was not performed when the Quasi-peak measured data under the limit of average detection.

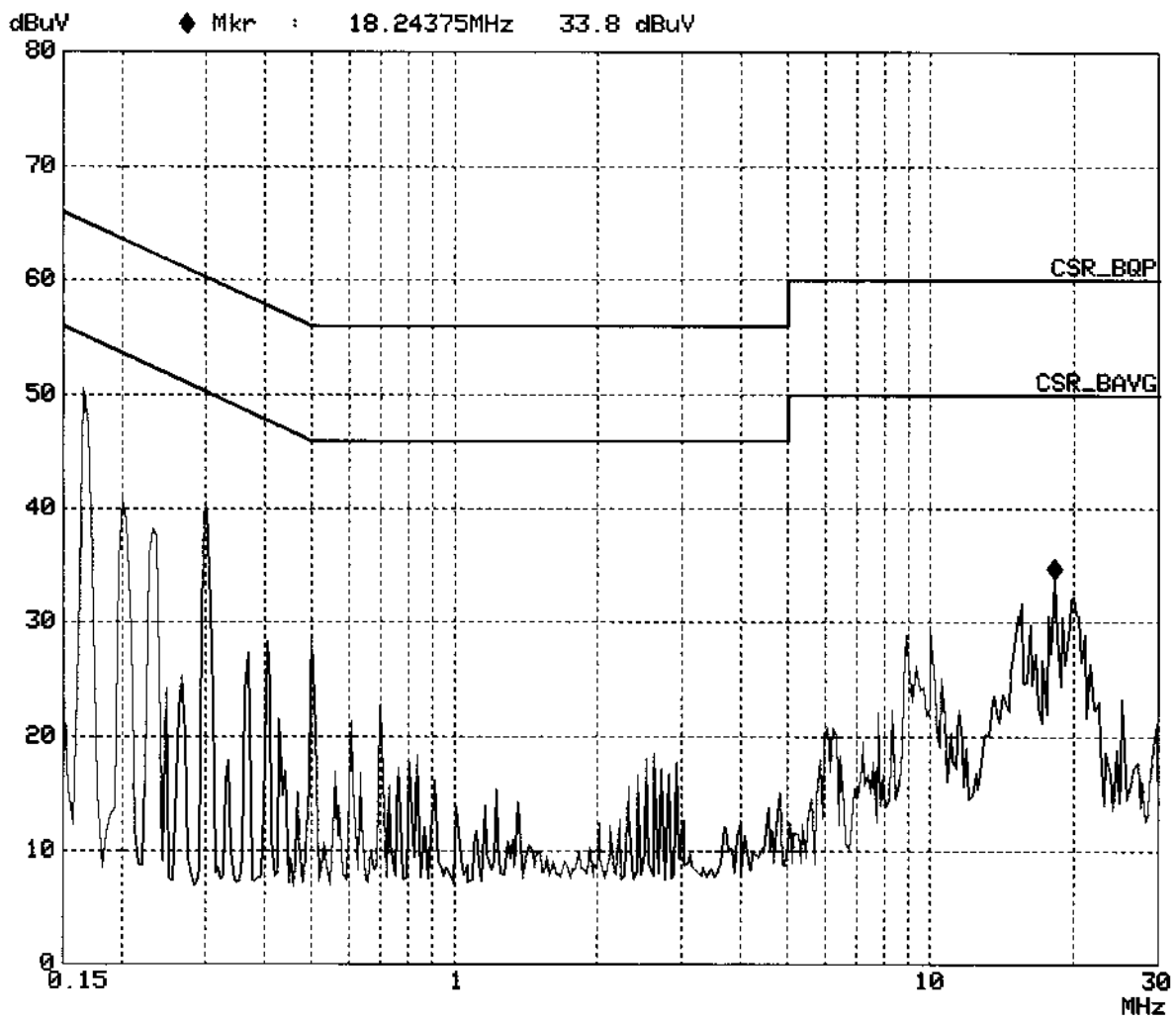
QUIETEK CORPORATION  
EMI TEST RECEIVER ESCS 30

EUT: VGA CARD  
Manuf: ASUS  
Op Cond: FULL SYSTEM  
Operator: MILLER  
Test Spec: 230Vac/50Hz  
Comment: LINE 1  
M/N: V8460 Deluxe, MODE:1  
File name: CISPR22B.SPC  
Date: 21. May 02 20:49



QUIETEK CORPORATION  
EMI TEST RECEIVER ESCS 30

EUT: VGA CARD  
Manuf: ASUS  
Op Cond: FULL SYSTEM  
Operator: MILLER  
Test Spec: 230Vac/50Hz  
Comment: LINE 2  
M/N: V8460 Deluxe, MODE:1  
File name: CISPR22B.SPC  
Date: 21. May 02 20:53



Date of Test	May 22, 2002	Test Room	No.4 Shielded Room
Test Mode	Mode 2: 1600*1200/85Hz, D-sub+S-video+AV	Product	VGA Card
Test Condition	Line1 & Line2	Test Range	0.15MHz – 30MHz

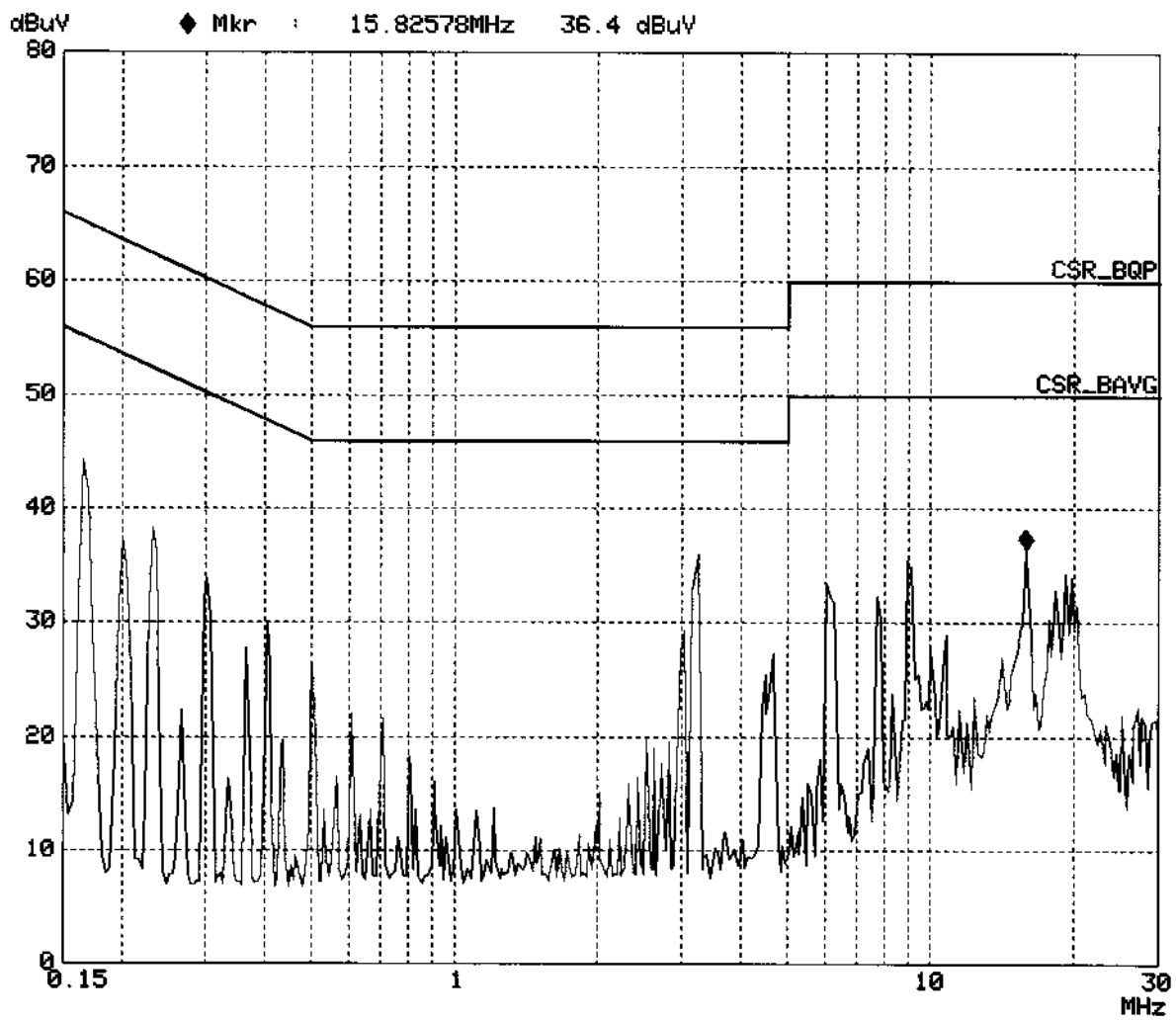
Frequency MHz	Measurement Level (dBuV)				Limits (dBuV)	
	Line1 QP	Line1 AV	Line2 QP	Line2 AV	QP	AV
0.166	44.31	--	--	--	65.18	55.18
0.166	--	--	50.89	--	65.18	55.18
0.231	39.13	--	--	--	62.43	52.43
0.233	--	--	42.07	--	62.34	52.34
0.302	35.43	--	--	--	60.19	50.19
0.302	--	--	40.55	--	60.19	50.19
6.112	--	--	25.86	--	60.00	50.00
6.113	27.20	--	--	--	60.00	50.00
8.336	--	--	22.88	--	60.00	50.00
8.890	30.60	--	--	--	60.00	50.00
16.167	30.27	--	--	--	60.00	50.00
16.167	--	--	29.51	--	60.00	50.00

## Note:

1. All Reading Levels are Quasi-Peak and average value.
2. Measurement Level = Reading Level + LISN Factor + Cable loss.
3. "--", means the average measurement was not performed when the Quasi-peak measured data under the limit of average detection.

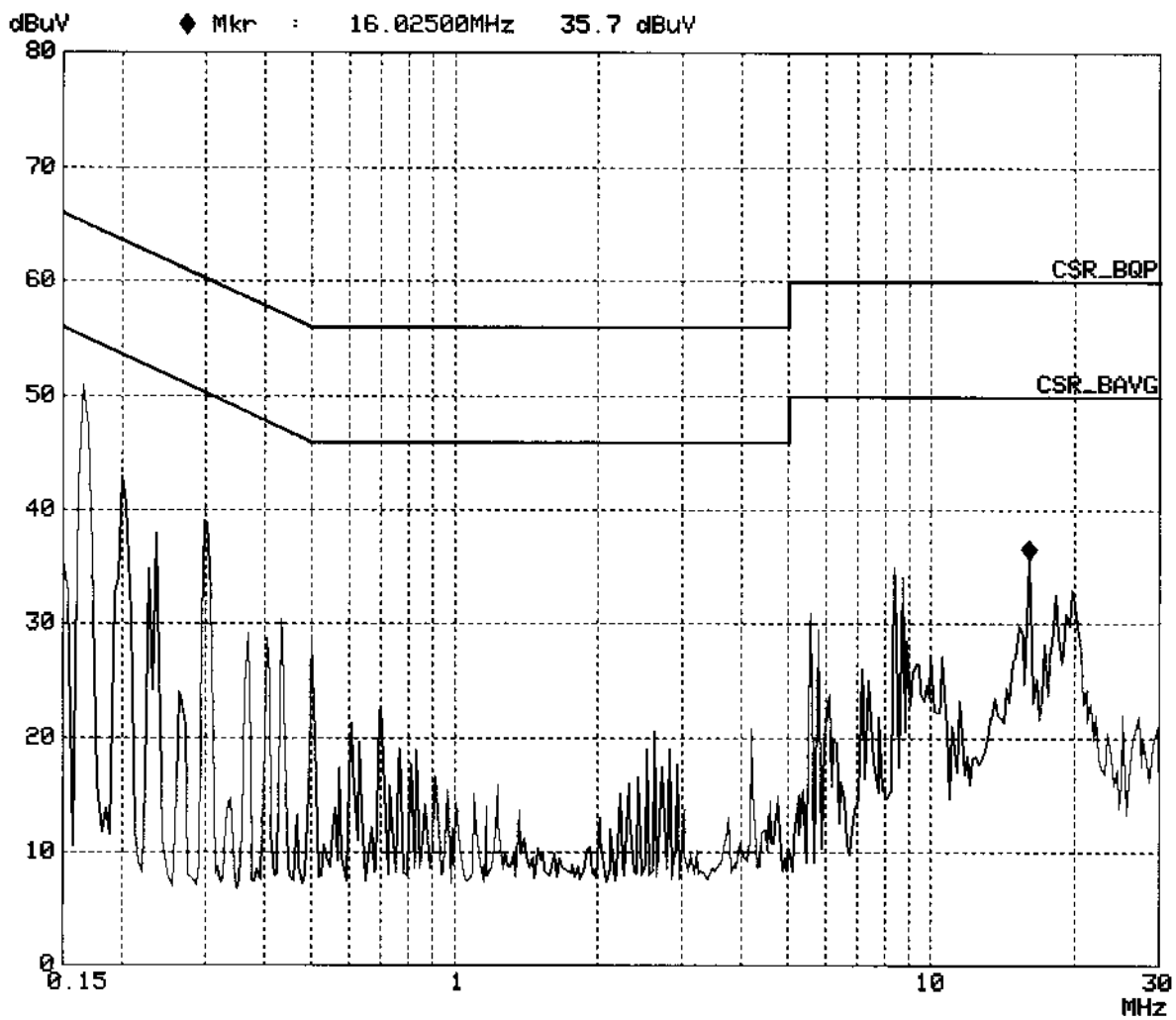
QUIETEK CORPORATION  
EMI TEST RECEIVER ESCS 30

EUT: VGA CARD  
Manuf: ASUS  
Op Cond: FULL SYSTEM  
Operator: MILLER  
Test Spec: 230Vac/50Hz  
Comment: LINE 1  
M/N: V8460 Deluxe, MODE: 2  
File name: CISPR22B.SPC  
Date: 21. May 02 20:59



QUIETEK CORPORATION  
EMI TEST RECEIVER ESCS 30

EUT: VGA CARD  
Manuf: ASUS  
Op Cond: FULL SYSTEM  
Operator: MILLER  
Test Spec: 230Vac/50Hz  
Comment: LINE 2  
M/N: V8460 Deluxe, MODE:2  
File name: CISPR22B.SPC  
Date: 21. May 02 21:04



Date of Test	May 22, 2002	Test Room	No.4 Shielded Room
Test Mode	Mode 3:1024*768/60Hz, DVI+S-video+AV	Product	VGA Card
Test Condition	Line1 & Line2	Test Range	0.15MHz – 30MHz

Frequency MHz	Measurement Level (dBuV)				Limits (dBuV)	
	Line1 QP	Line1 AV	Line2 QP	Line2 AV	QP	AV
0.166	44.43	--	--	--	65.18	55.18
0.166	--	--	51.01	--	65.18	55.18
0.233	39.81	--	--	--	62.34	52.34
0.233	--	--	42.09	--	62.34	52.34
0.302	--	--	40.71	--	60.19	50.19
0.304	34.89	--	--	--	60.14	50.14
0.404	30.47	--	--	--	57.78	47.78
0.432	--	--	31.43	--	57.21	47.21
8.892	29.16	--	--	--	60.00	50.00
10.005	--	--	28.61	--	60.00	50.00
18.242	35.34	--	--	--	60.00	50.00
18.242	--	--	34.82	--	60.00	50.00

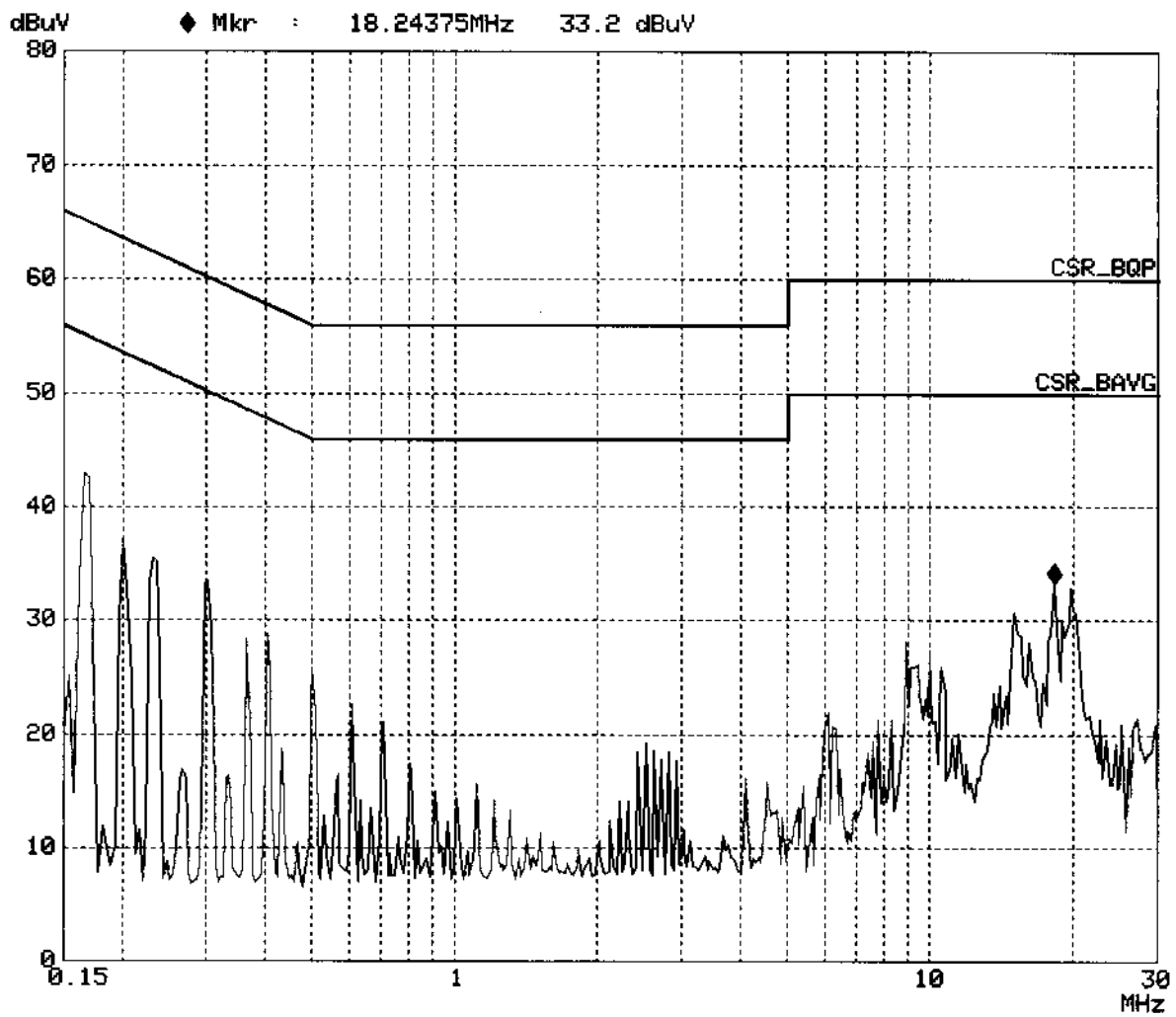
## Note:

1. All Reading Levels are Quasi-Peak and average value.
2. Measurement Level = Reading Level + LISN Factor + Cable loss.
3. "--", means the average measurement was not performed when the Quasi-peak measured data under the limit of average detection.



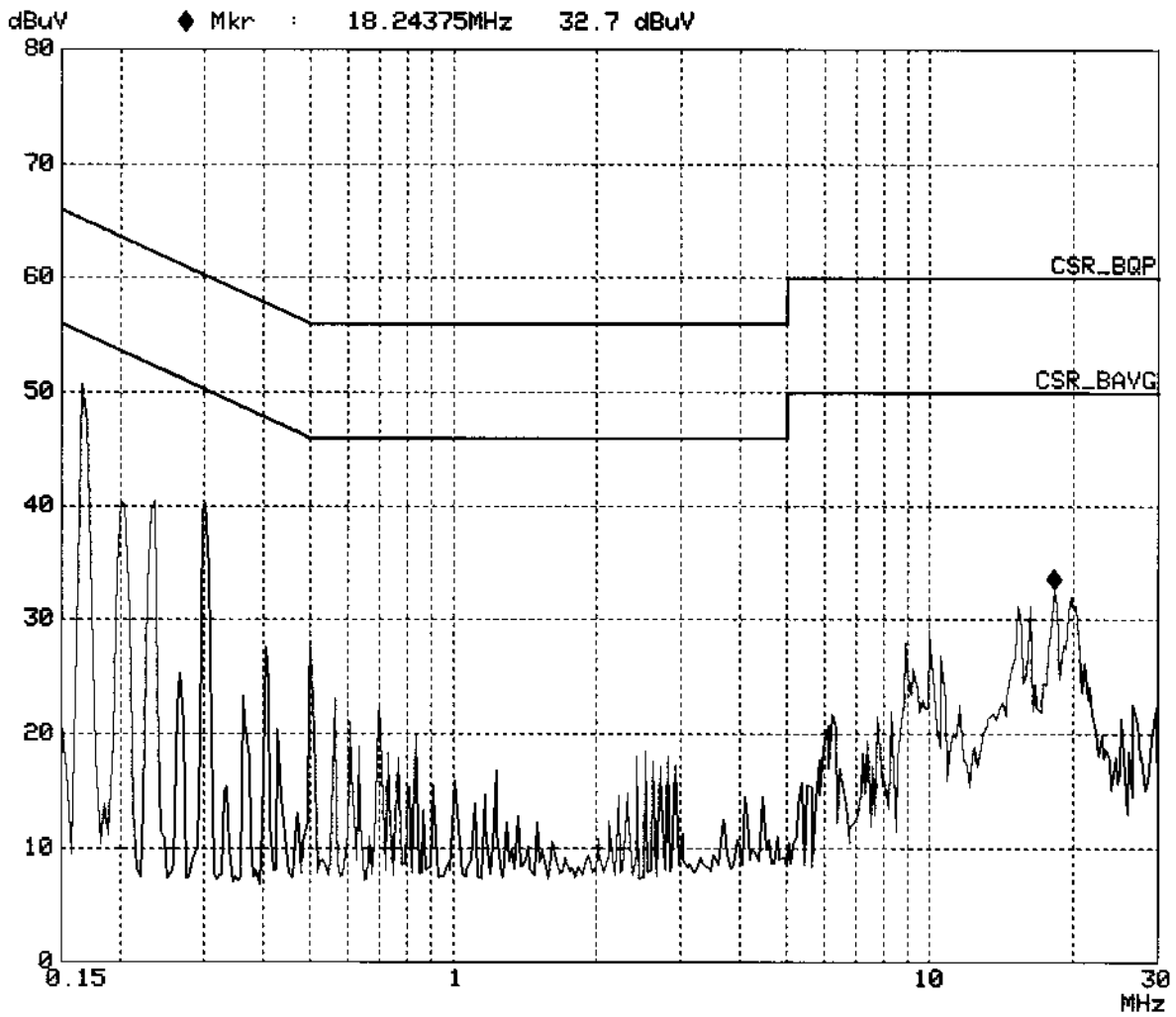
QUIETEK CORPORATION  
EMI TEST RECEIVER ESCS 30

EUT: VGA CARD  
Manuf: ASUS  
Op Cond: FULL SYSTEM  
Operator: MILLER  
Test Spec: 230Vac/50Hz  
Comment: LINE 1  
M/N: V8460 Deluxe, MODE:3  
File name: CISPR22B.SPC  
Date: 21. May 02 21:10



QUIETEK CORPORATION  
EMI TEST RECEIVER ESCS 30

EUT: VGA CARD  
Manuf: ASUS  
Op Cond: FULL SYSTEM  
Operator: MILLER  
Test Spec: 230Vac/50Hz  
Comment: LINE 2  
M/N: V8460 Deluxe, MODE:3  
File name: CISPR22B.SPC  
Date: 21. May 02 21:16



Date of Test	May 22, 2002	Test Room	No.4 Shielded Room
Test Mode	Mode 4:1024*768/60Hz, D-sub+S-video(INPUT)	Product	VGA Card
Test Condition	Line1 & Line2	Test Range	0.15MHz – 30MHz

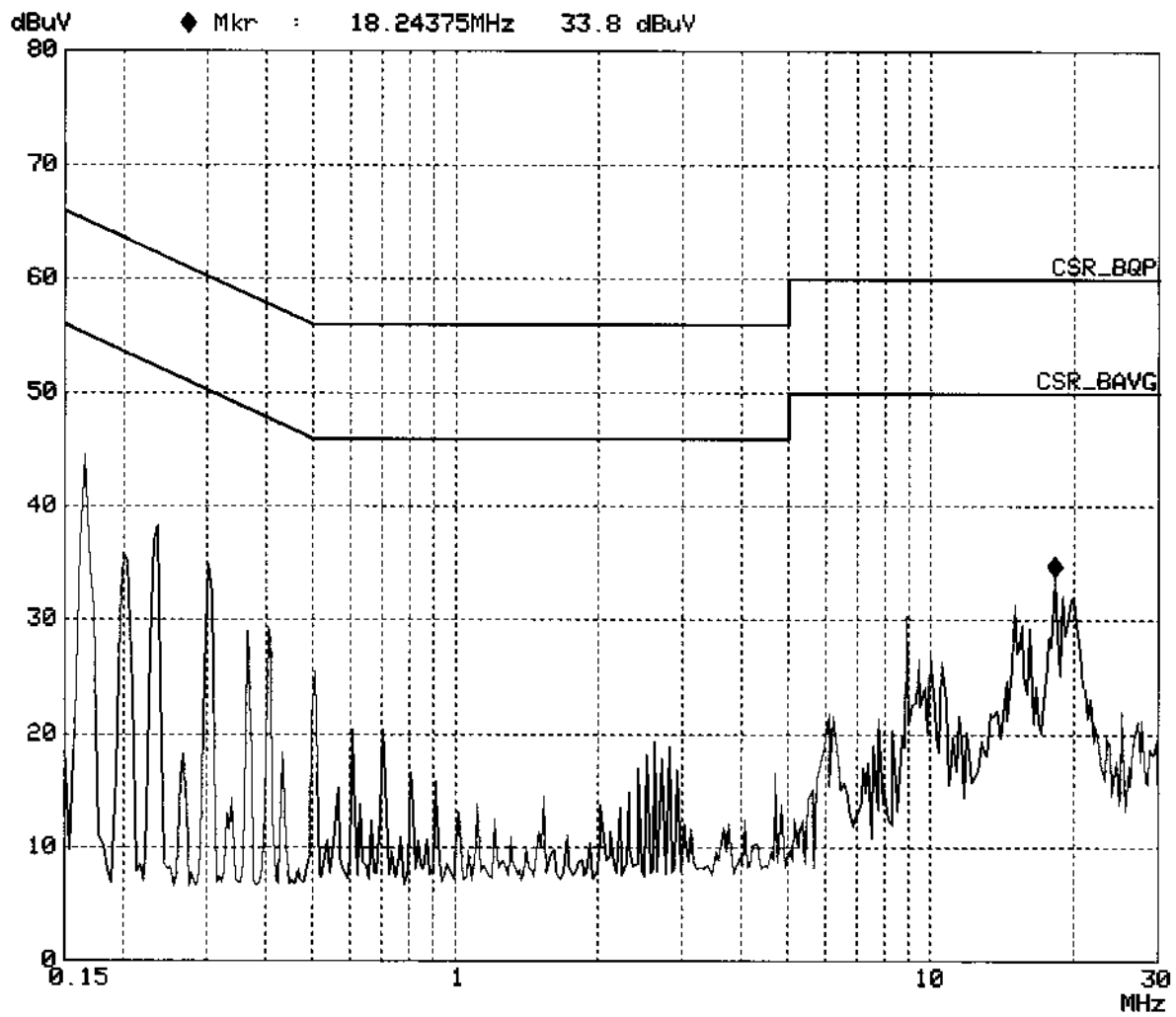
Frequency MHz	Measurement Level (dBuV)				Limits (dBuV)	
	Line1 QP	Line1 AV	Line2 QP	Line2 AV	QP	AV
0.166	44.55	--	--	--	65.18	55.18
0.166	--	--	51.01	--	65.17	55.17
0.231	38.83	--	--	--	62.43	52.43
0.232	--	--	42.21	--	62.38	52.38
0.302	35.67	--	--	--	60.19	50.19
0.302	--	--	40.75	--	60.19	50.19
0.403	--	--	28.45	--	57.79	47.79
0.405	30.25	--	--	--	57.76	47.76
8.895	30.20	--	--	--	60.00	50.00
8.895	--	--	29.68	--	60.00	50.00
18.241	--	--	34.90	--	60.00	50.00
18.242	35.08	--	--	--	60.00	50.00

Note:

1. All Reading Levels are Quasi-Peak and average value.
2. Measurement Level = Reading Level + LISN Factor + Cable loss.
3. "--", means the average measurement was not performed when the Quasi-peak measured data under the limit of average detection.

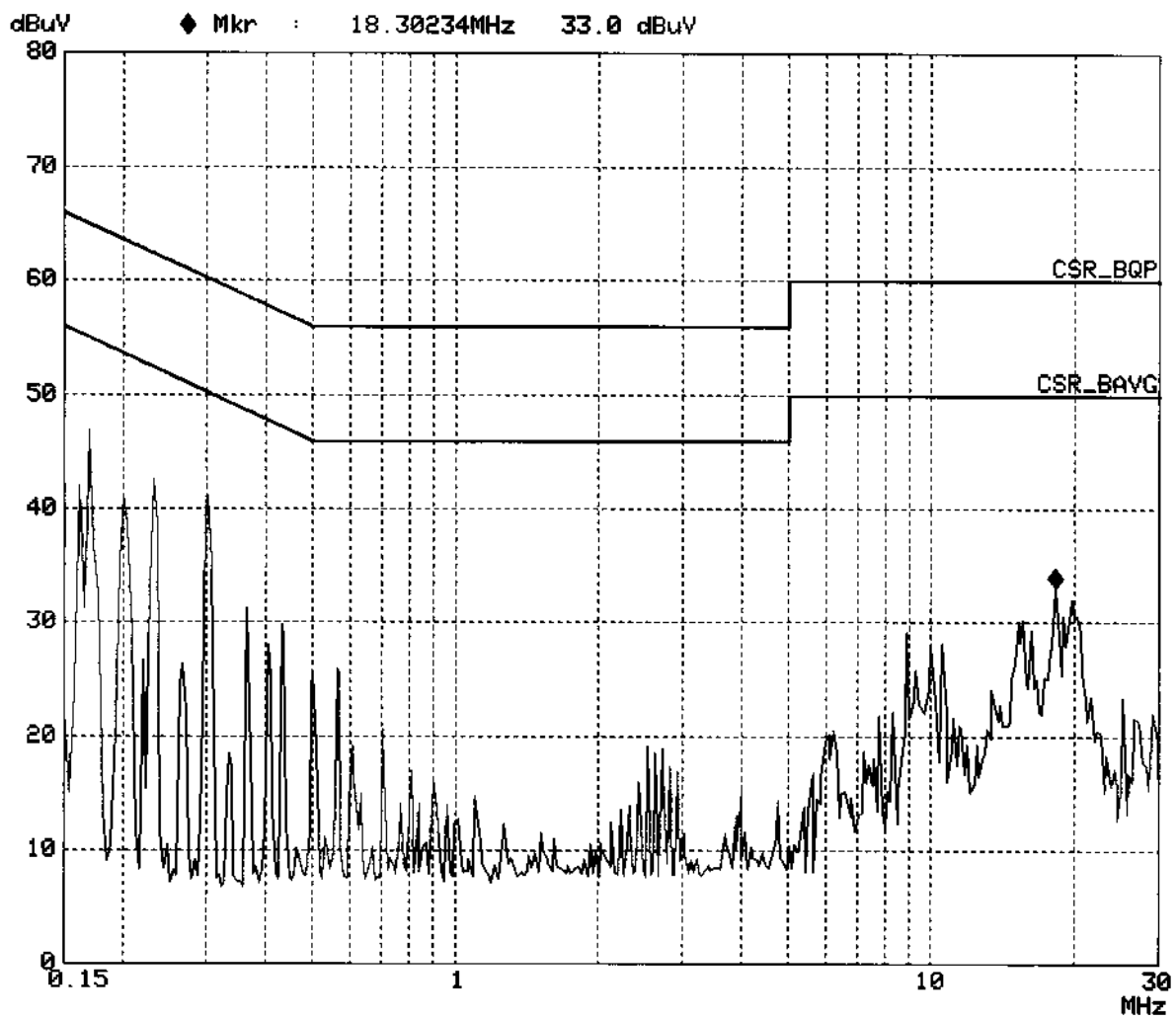
QUIETEK CORPORATION  
EMI TEST RECEIVER ESCS 30

EUT: VGA CARD  
Manuf: ASUS  
Op Cond: FULL SYSTEM  
Operator: MILLER  
Test Spec: 230Vac/50Hz  
Comment: LINE 1  
M/N: V8460 Deluxe, MODE: 4  
File name: CISPR22B.SPC  
Date: 21. May 02 21:20



QUIETEK CORPORATION  
EMI TEST RECEIVER ESCS 30

EUT: VGA CARD  
Manuf: ASUS  
Op Cond: FULL SYSTEM  
Operator: MILLER  
Test Spec: 230Vac/50Hz  
Comment: LINE 2  
M/N: V8460 Deluxe, MODE:4  
File name: CISPR22B.SPC  
Date: 21. May 02 21:24



Date of Test	May 22, 2002	Test Room	No.4 Shielded Room
Test Mode	Mode 5: 800*600/60Hz, D-sub+AV(INPUT)	Product	VGA Card
Test Condition	Line1 & Line2	Test Range	0.15MHz – 30MHz

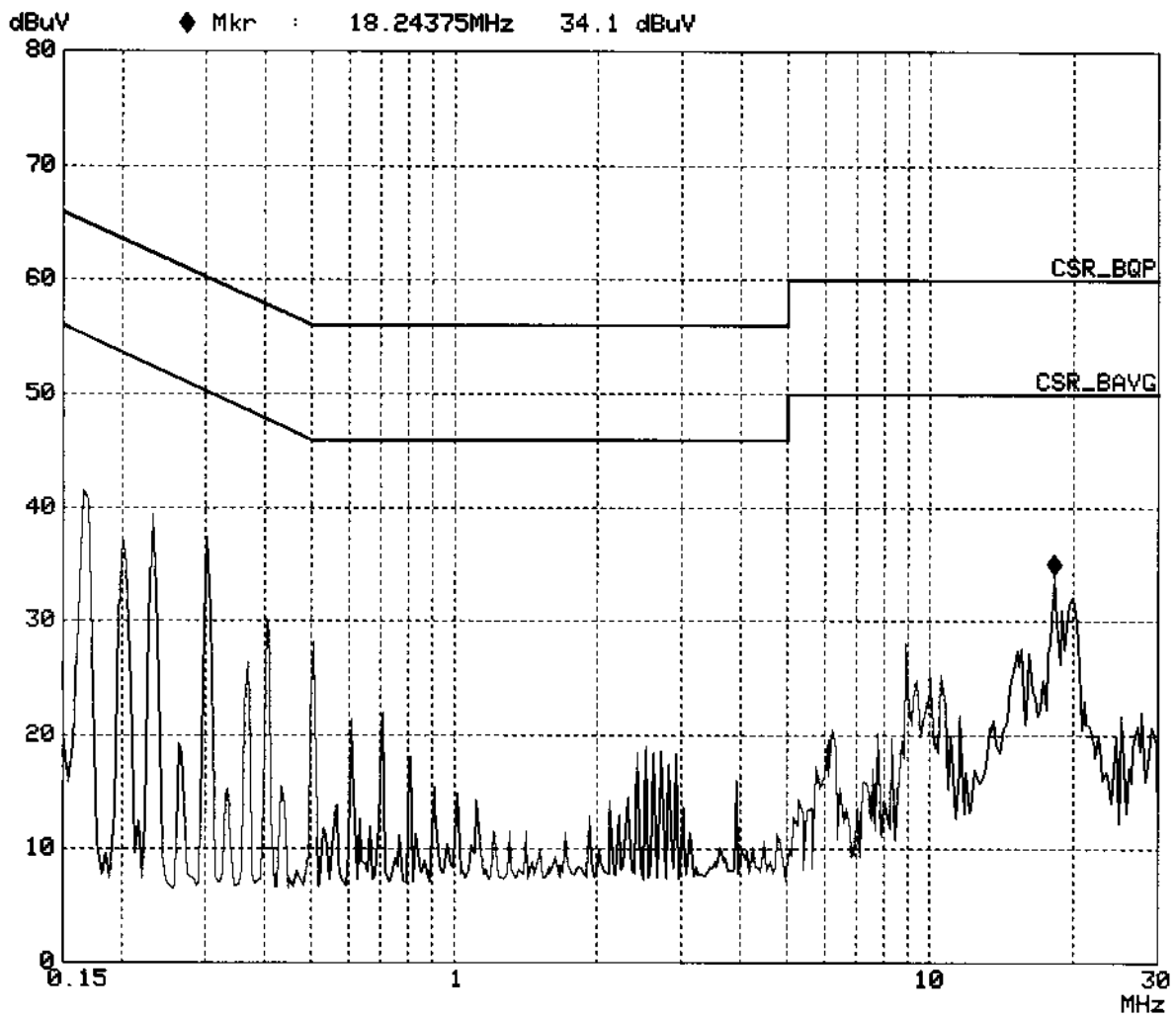
Frequency MHz	Measurement Level (dBuV)				Limits (dBuV)	
	Line1 QP	Line1 AV	Line2 QP	Line2 AV	QP	AV
0.166	44.57	--	--	--	65.18	55.18
0.166	--	--	51.01	--	65.18	55.18
0.231	38.85	--	--	--	62.43	52.43
0.234	--	--	42.03	--	62.32	52.32
0.301	35.19	--	--	--	60.22	50.22
0.301	--	--	40.83	--	60.21	50.21
0.403	30.53	--	--	--	57.79	47.79
0.434	--	--	31.15	--	57.18	47.18
8.894	30.06	--	--	--	60.00	50.00
10.005	--	--	30.25	--	60.00	50.00
18.241	--	--	35.84	--	60.00	50.00
18.242	35.10	--	--	--	60.00	50.00

Note:

1. All Reading Levels are Quasi-Peak and average value.
2. Measurement Level = Reading Level + LISN Factor + Cable loss.
3. "--", means the average measurement was not performed when the Quasi-peak measured data under the limit of average detection.

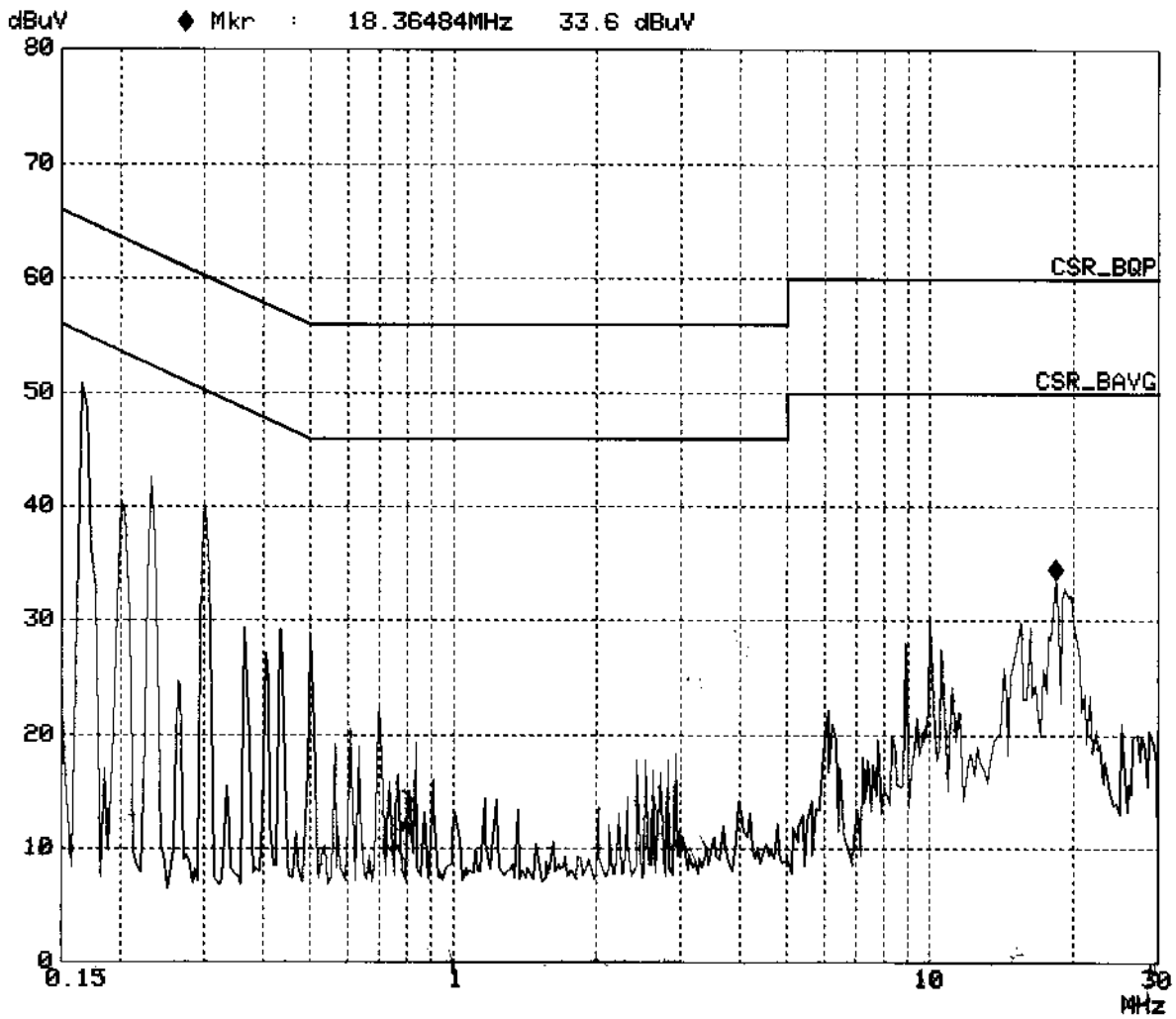
QUIETEK CORPORATION  
EMI TEST RECEIVER ESCS 30

EUT: VGA CARD  
Manuf: ASUS  
Op Cond: FULL SYSTEM  
Operator: MILLER  
Test Spec: 230Vac/50Hz  
Comment: LINE 1  
M/N: V8460 Deluxe, MODE:5  
File name: CISPR22B.SPC  
Date: 21. May 02 21:28



QUIETEK CORPORATION  
EMI TEST RECEIVER ESCS 30

EUT: VGA CARD  
Manuf: ASUS  
Op Cond: FULL SYSTEM  
Operator: MILLER  
Test Spec: 230Vac/50Hz  
Comment: LINE 2  
M/N: V8460 Deluxe, MODE:5  
File name: CISPR22B.SPC  
Date: 21. May 02 21:33





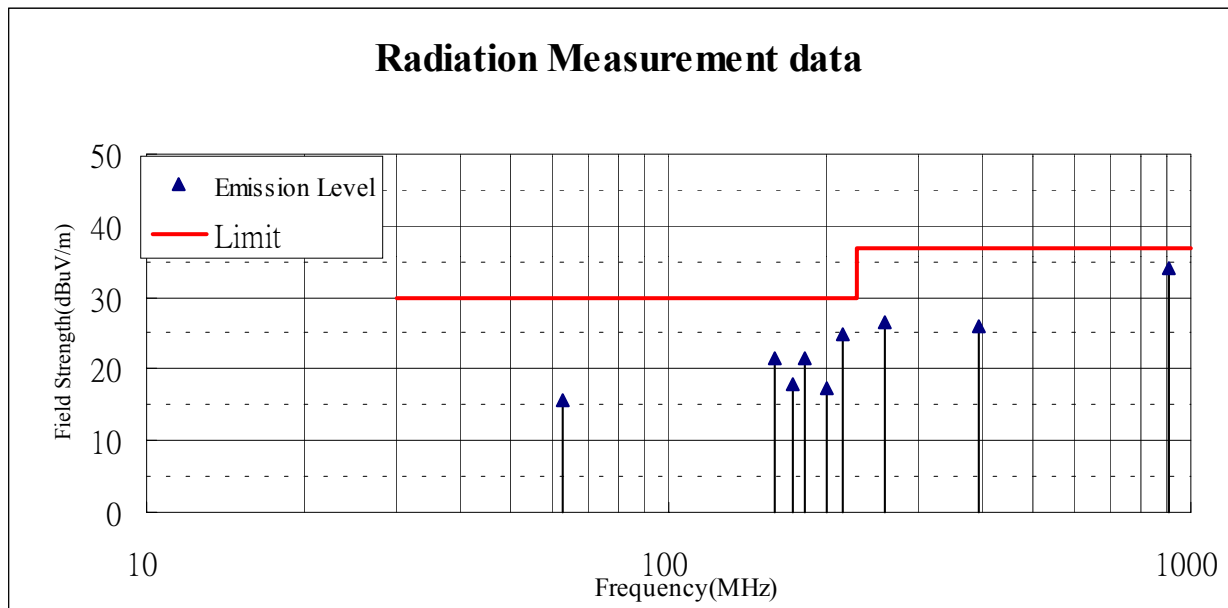
**13.2. Test Data of Radiated Emission**

Date of Test	May 22, 2002	Test Site	No.3 OATS
Test Mode	Mode 1: 1920*1440/75Hz, D-sub+DVI	Product	VGA Card
Test Condition	10m & Horizontal	Test Range	30MHz – 1GHz

Frequency MHz	Cable Loss (dB)	Probe Factor (dB/m)	Pre-Amp Factor (dB)	Reading Level (dBuV/m)	Emission Level (dBuV/m)	Margin (dB)	Limit (dBuV/m)
62.670	1.04	5.65	0.00	8.89	15.58	-14.42	30.00
160.041	1.54	9.62	0.00	10.47	21.63	-8.37	30.00
173.419	1.61	8.69	0.00	7.57	17.87	-12.13	30.00
182.889	1.66	8.12	0.00	11.84	21.62	-8.38	30.00
200.140	1.74	8.40	0.00	7.10	17.24	-12.76	30.00
215.979	1.83	8.21	0.00	14.76	24.80	-5.20	30.00
260.132	2.05	12.95	0.00	11.67	26.67	-10.33	37.00
392.905	2.74	14.46	0.00	8.78	25.98	-11.02	37.00
910.453	5.40	19.54	0.00	9.21	34.15	-2.85	37.00

Note:

1. All Reading Levels below 1GHz are Quasi-Peak.
2. Emission Level = Reading Level + Probe Factor + Cable loss – Preamp.
3. Margin = Limit - Emission Level

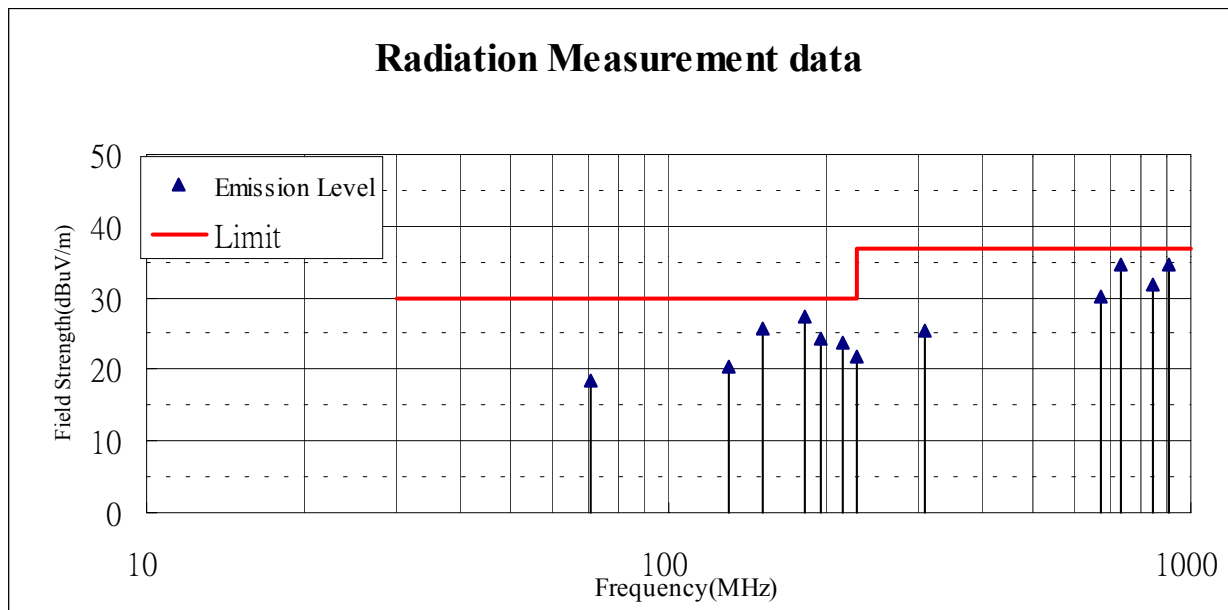


Date of Test	May 22, 2002	Test Site	No.3 OATS
Test Mode	Mode 1: 1920*1440/75Hz, D-sub+DVI	Product	VGA Card
Test Condition	10m & Vertical	Test Range	30MHz – 1GHz

Frequency MHz	Cable Loss (dB)	Probe Factor (dB/m)	Pre-Amp Factor (dB)	Reading Level (dBuV/m)	Emission Level (dBuV/m)	Margin (dB)	Limit (dBuV/m)
71.030	1.08	6.58	0.00	10.84	18.50	-11.50	30.00
130.067	1.38	10.59	0.00	8.56	20.53	-9.47	30.00
151.742	1.49	9.28	0.00	14.79	25.56	-4.44	30.00
182.775	1.65	8.36	0.00	17.37	27.38	-2.62	30.00
195.098	1.72	8.11	0.00	14.54	24.37	-5.63	30.00
215.985	1.83	9.33	0.00	12.69	23.85	-6.15	30.00
228.469	1.89	9.73	0.00	10.28	21.90	-8.10	30.00
308.710	2.31	12.17	0.00	10.98	25.46	-11.54	37.00
674.939	4.19	17.70	0.00	8.28	30.17	-6.83	37.00
731.766	4.49	20.51	0.00	9.52	34.52	-2.48	37.00
845.423	5.07	18.88	0.00	7.76	31.71	-5.29	37.00
910.454	5.40	21.14	0.00	8.11	34.65	-2.35	37.00

Note:

1. All Reading Levels below 1GHz are Quasi-Peak.
2. Emission Level = Reading Level + Probe Factor + Cable loss – Preamp.
3. Margin = Limit - Emission Level

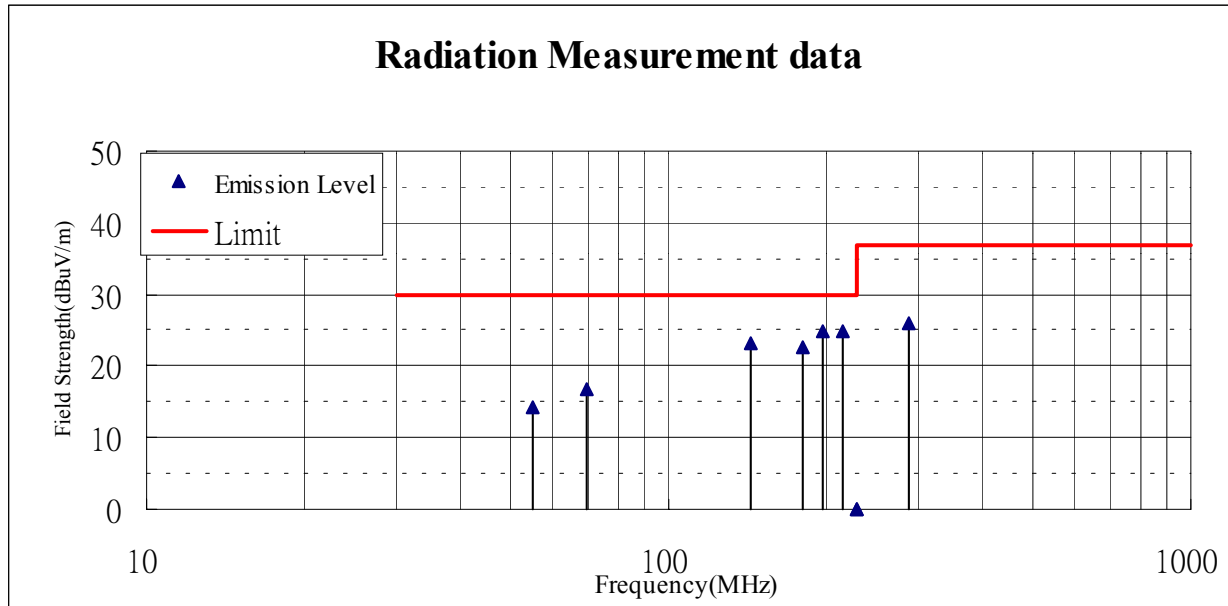


Date of Test	May 22, 2002	Test Site	No.3 OATS
Test Mode	Mode 2: 1600*1200/85Hz, D-sub+S-video+AV	Product	VGA Card
Test Condition	10m & Horizontal	Test Range	30MHz – 1GHz

Frequency MHz	Cable Loss (dB)	Probe Factor (dB/m)	Pre-Amp Factor (dB)	Reading Level (dBuV/m)	Emission Level (dBuV/m)	Margin (dB)	Limit (dBuV/m)
55.010	1.00	6.04	0.00	7.12	14.16	-15.84	30.00
69.780	1.07	6.12	0.00	9.60	16.79	-13.21	30.00
143.931	1.45	10.87	0.00	10.86	23.18	-6.82	30.00
179.995	1.64	8.27	0.00	12.68	22.59	-7.41	30.00
197.922	1.73	8.25	0.00	14.81	24.79	-5.21	30.00
215.985	1.83	8.21	0.00	14.88	24.92	-5.08	30.00
287.997	2.20	11.91	0.00	11.80	25.91	-11.09	37.00

Note:

1. All Reading Levels below 1GHz are Quasi-Peak.
2. Emission Level = Reading Level + Probe Factor + Cable loss – Preamp.
3. Margin = Limit - Emission Level

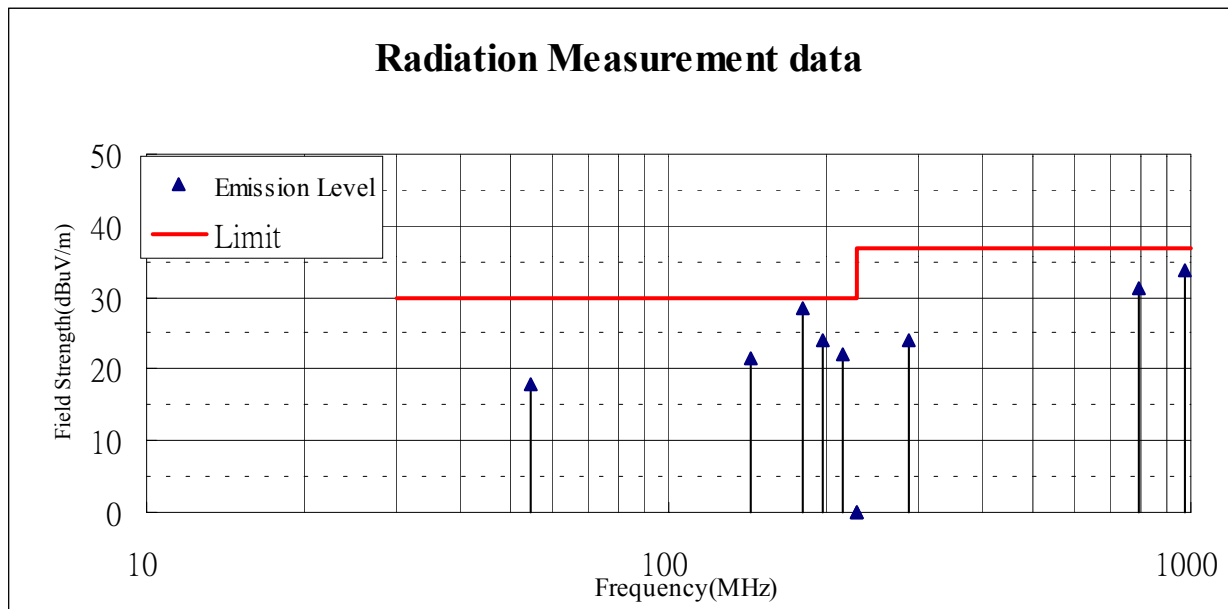


Date of Test	May 22, 2002	Test Site	No.3 OATS
Test Mode	Mode 2: 1600*1200/85Hz, D-sub+S-video+AV	Product	VGA Card
Test Condition	10m & Vertical	Test Range	30MHz – 1GHz

Frequency MHz	Cable Loss (dB)	Probe Factor (dB/m)	Pre-Amp Factor (dB)	Reading Level (dBuV/m)	Emission Level (dBuV/m)	Margin (dB)	Limit (dBuV/m)
54.640	0.99	5.90	0.00	10.90	17.79	-12.21	30.00
143.939	1.45	9.96	0.00	10.08	21.49	-8.51	30.00
179.997	1.64	8.41	0.00	18.32	28.37	-1.63	30.00
197.922	1.73	8.27	0.00	14.12	24.12	-5.88	30.00
215.980	1.83	9.33	0.00	10.98	22.14	-7.86	30.00
287.988	2.20	12.27	0.00	9.59	24.06	-12.94	37.00
798.230	4.83	19.34	0.00	7.12	31.29	-5.71	37.00
971.907	5.72	19.98	0.00	8.16	33.86	-3.14	37.00

Note:

1. All Reading Levels below 1GHz are Quasi-Peak.
2. Emission Level = Reading Level + Probe Factor + Cable loss – Preamp.
3. Margin = Limit - Emission Level

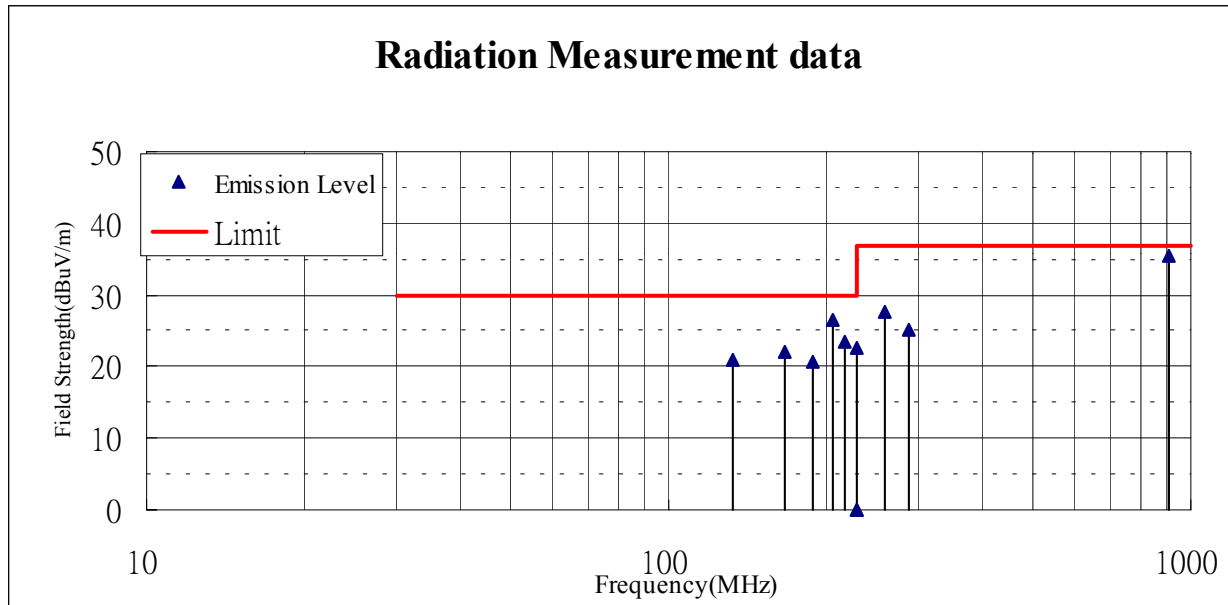


Date of Test	May 22, 2002	Test Site	No.3 OATS
Test Mode	Mode 3:1024*768/60Hz, DVI+S-video+AV	Product	VGA Card
Test Condition	10m & Horizontal	Test Range	30MHz – 1GHz

Frequency MHz	Cable Loss (dB)	Probe Factor (dB/m)	Pre-Amp Factor (dB)	Reading Level (dBuV/m)	Emission Level (dBuV/m)	Margin (dB)	Limit (dBuV/m)
132.986	1.40	11.49	0.00	8.07	20.96	-9.04	30.00
167.398	1.57	9.09	0.00	11.46	22.12	-7.88	30.00
188.996	1.69	8.00	0.00	10.96	20.65	-9.35	30.00
205.936	1.77	8.61	0.00	16.25	26.63	-3.37	30.00
216.780	1.83	8.15	0.00	13.56	23.54	-6.46	30.00
229.081	1.89	9.52	0.00	11.23	22.64	-7.36	30.00
260.130	2.05	12.95	0.00	12.53	27.53	-9.47	37.00
287.999	2.20	11.91	0.00	11.05	25.16	-11.84	37.00
910.458	5.40	19.54	0.00	10.43	35.37	-1.63	37.00

Note:

1. All Reading Levels below 1GHz are Quasi-Peak.
2. Emission Level = Reading Level + Probe Factor + Cable loss – Preamp.
3. Margin = Limit - Emission Level

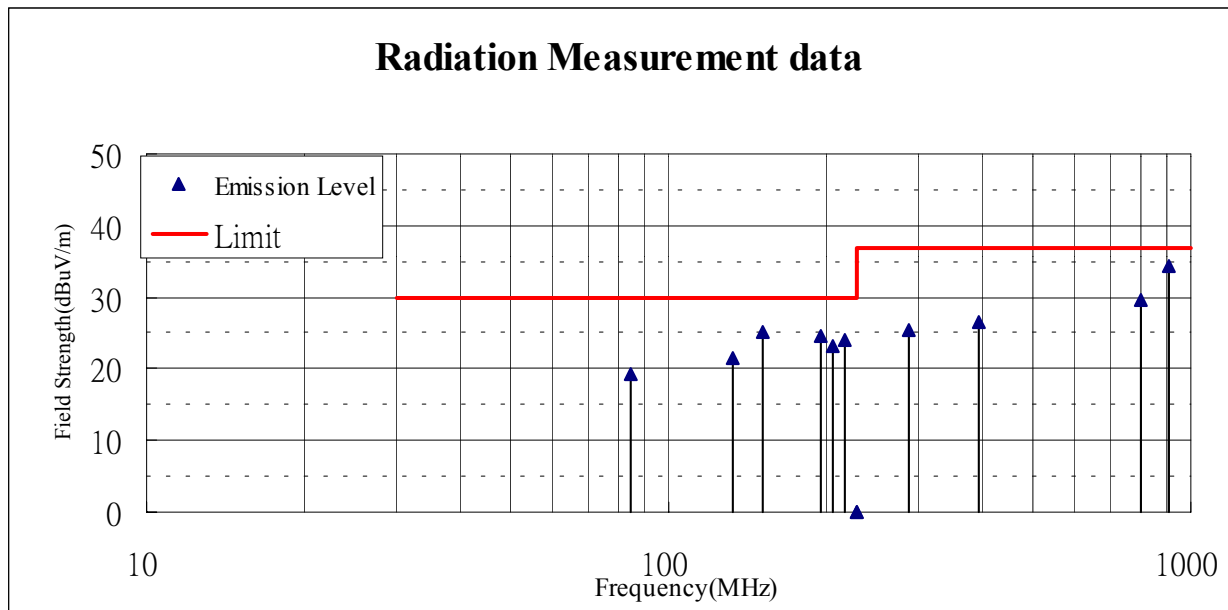


Date of Test	May 22, 2002	Test Site	No.3 OATS
Test Mode	Mode 3:1024*768/60Hz, DVI+S-video+AV	Product	VGA Card
Test Condition	10m & Vertical	Test Range	30MHz – 1GHz

Frequency MHz	Cable Loss (dB)	Probe Factor (dB/m)	Pre-Amp Factor (dB)	Reading Level (dBuV/m)	Emission Level (dBuV/m)	Margin (dB)	Limit (dBuV/m)
84.340	1.15	7.66	0.00	10.37	19.18	-10.82	30.00
132.970	1.40	10.55	0.00	9.63	21.58	-8.42	30.00
151.742	1.49	9.28	0.00	14.28	25.05	-4.95	30.00
195.096	1.72	8.11	0.00	14.70	24.53	-5.47	30.00
205.938	1.77	8.69	0.00	12.61	23.07	-6.93	30.00
216.777	1.83	9.26	0.00	12.99	24.08	-5.92	30.00
287.989	2.20	12.27	0.00	10.99	25.46	-11.54	37.00
392.906	2.74	15.79	0.00	7.88	26.41	-10.59	37.00
802.058	4.85	19.26	0.00	5.40	29.51	-7.49	37.00
910.453	5.40	21.14	0.00	7.74	34.28	-2.72	37.00

Note:

1. All Reading Levels below 1GHz are Quasi-Peak.
2. Emission Level = Reading Level + Probe Factor + Cable loss – Preamp.
3. Margin = Limit - Emission Level

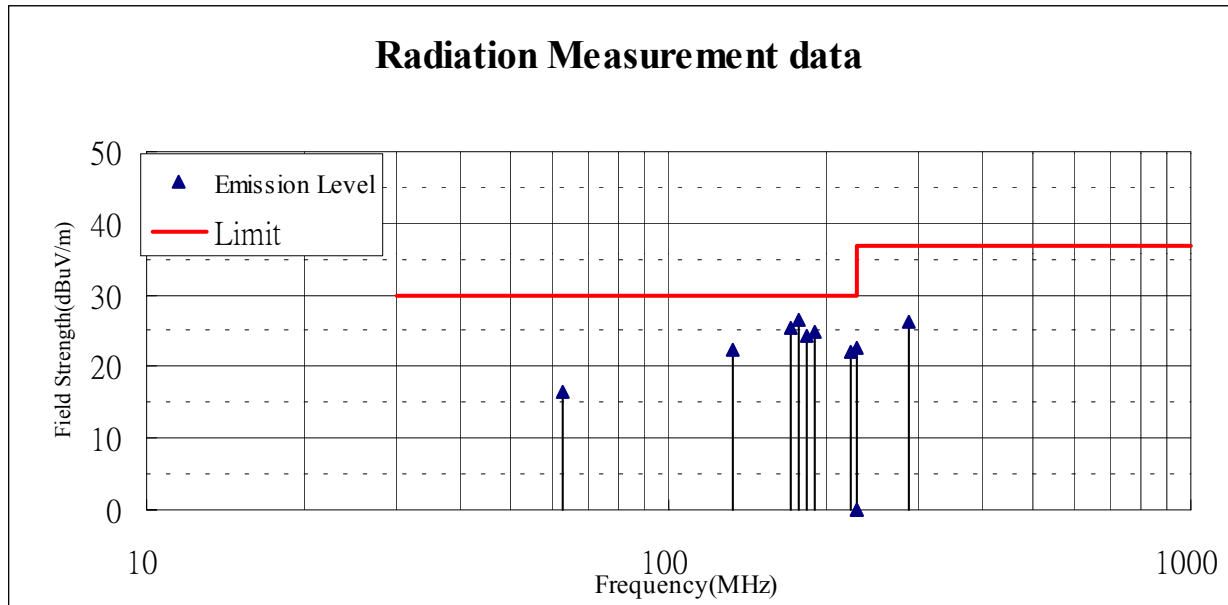


Date of Test	May 22, 2002	Test Site	No.3 OATS
Test Mode	Mode 4:1024*768/60Hz, D-sub+S-video(INPUT)	Product	VGA Card
Test Condition	10m & Horizontal	Test Range	30MHz – 1GHz

Frequency MHz	Cable Loss (dB)	Probe Factor (dB/m)	Pre-Amp Factor (dB)	Reading Level (dBuV/m)	Emission Level (dBuV/m)	Margin (dB)	Limit (dBuV/m)
62.620	1.04	5.65	0.00	9.90	16.59	-13.41	30.00
132.973	1.40	11.49	0.00	9.34	22.23	-7.77	30.00
170.657	1.59	8.93	0.00	14.78	25.30	-4.70	30.00
177.200	1.63	8.53	0.00	16.31	26.47	-3.53	30.00
183.741	1.66	8.02	0.00	14.69	24.37	-5.63	30.00
190.343	1.69	8.16	0.00	14.92	24.77	-5.23	30.00
222.697	1.86	8.78	0.00	11.33	21.97	-8.03	30.00
229.083	1.89	9.52	0.00	11.27	22.68	-7.32	30.00
287.993	2.20	11.91	0.00	12.02	26.13	-10.87	37.00

Note:

1. All Reading Levels below 1GHz are Quasi-Peak.
2. Emission Level = Reading Level + Probe Factor + Cable loss – Preamp.
3. Margin = Limit - Emission Level

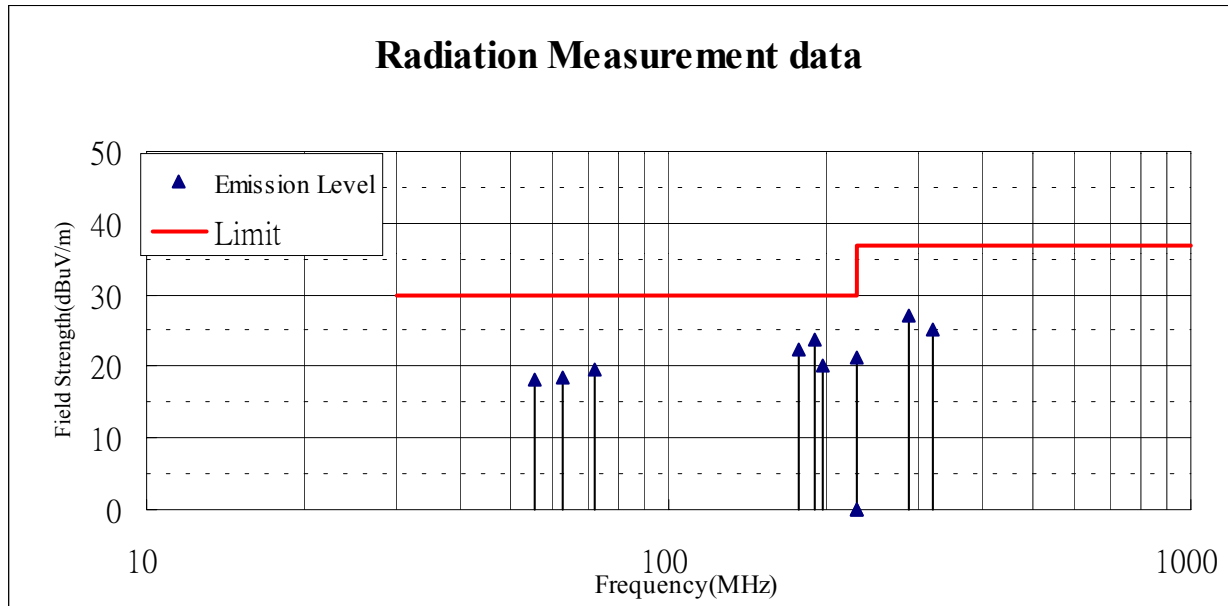


Date of Test	May 22, 2002	Test Site	No.3 OATS
Test Mode	Mode 4:1024*768/60Hz, D-sub+S-video(INPUT)	Product	VGA Card
Test Condition	10m & Vertical	Test Range	30MHz – 1GHz

Frequency MHz	Cable Loss (dB)	Probe Factor (dB/m)	Pre-Amp Factor (dB)	Reading Level (dBuV/m)	Emission Level (dBuV/m)	Margin (dB)	Limit (dBuV/m)
55.260	1.00	5.80	0.00	11.49	18.29	-11.71	30.00
62.870	1.04	5.78	0.00	11.53	18.35	-11.65	30.00
71.870	1.08	6.60	0.00	11.83	19.51	-10.49	30.00
177.200	1.63	8.45	0.00	12.25	22.33	-7.67	30.00
190.343	1.69	8.25	0.00	13.85	23.79	-6.21	30.00
197.945	1.73	8.27	0.00	10.24	20.24	-9.76	30.00
229.078	1.89	9.73	0.00	9.65	21.27	-8.73	30.00
287.987	2.20	12.27	0.00	12.66	27.13	-9.87	37.00
321.658	2.37	12.66	0.00	10.18	25.21	-11.79	37.00

Note:

1. All Reading Levels below 1GHz are Quasi-Peak.
2. Emission Level = Reading Level + Probe Factor + Cable loss – Preamp.
3. Margin = Limit - Emission Level



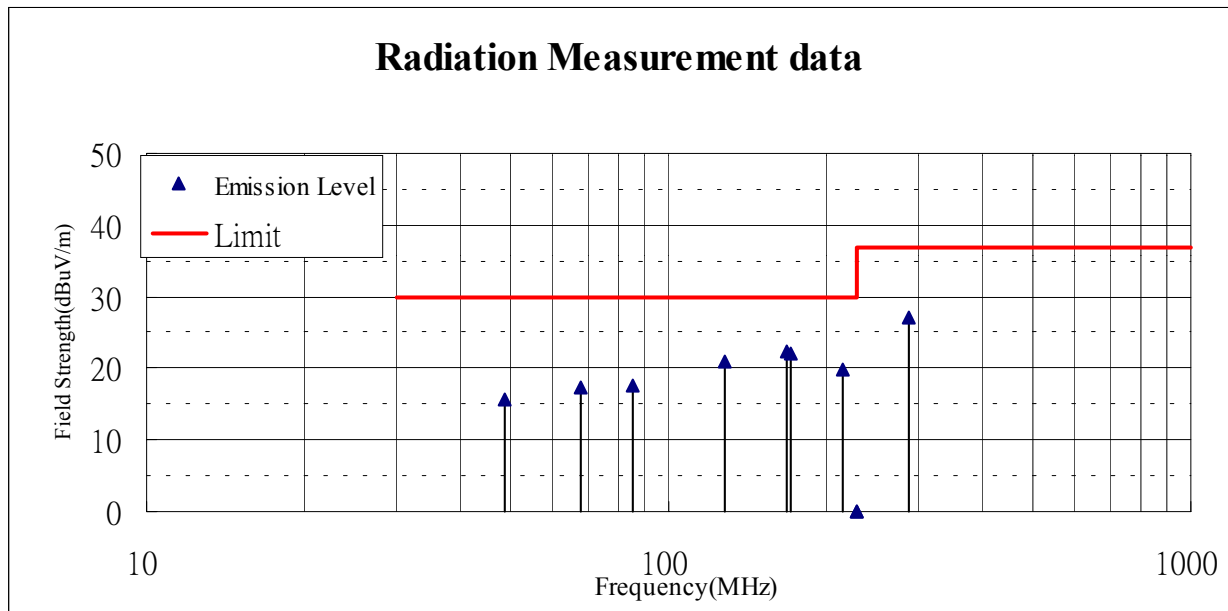


Date of Test	May 22, 2002	Test Site	No.3 OATS
Test Mode	Mode 5: 800*600/60Hz, D-sub+AV(INPUT)	Product	VGA Card
Test Condition	10m & Horizontal	Test Range	30MHz – 1GHz

Frequency MHz	Cable Loss (dB)	Probe Factor (dB/m)	Pre-Amp Factor (dB)	Reading Level (dBuV/m)	Emission Level (dBuV/m)	Margin (dB)	Limit (dBuV/m)
48.420	0.96	8.36	0.00	6.41	15.73	-14.27	30.00
67.740	1.06	5.99	0.00	10.24	17.29	-12.71	30.00
85.080	1.15	8.68	0.00	7.65	17.48	-12.52	30.00
127.972	1.37	11.69	0.00	7.81	20.87	-9.13	30.00
167.886	1.58	8.99	0.00	11.67	22.24	-7.76	30.00
171.899	1.60	8.93	0.00	11.43	21.96	-8.04	30.00
215.984	1.83	8.21	0.00	9.66	19.70	-10.30	30.00
287.995	2.20	11.91	0.00	13.01	27.12	-9.88	37.00

Note:

1. All Reading Levels below 1GHz are Quasi-Peak.
2. Emission Level = Reading Level + Probe Factor + Cable loss – Preamp.
3. Margin = Limit - Emission Level

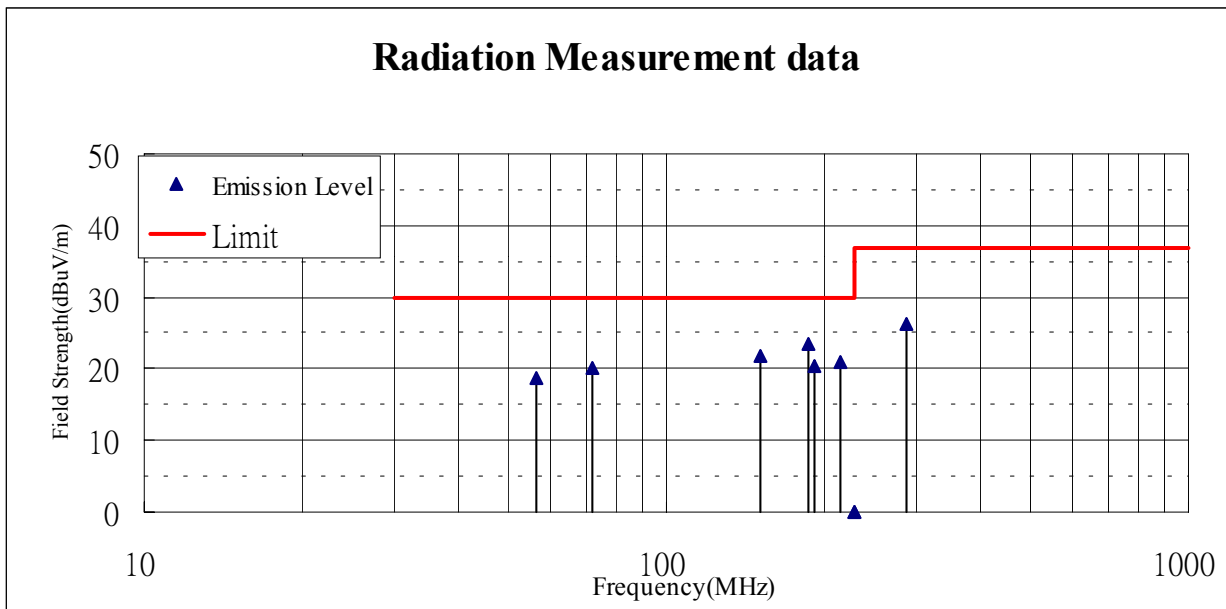


Date of Test	May 22, 2002	Test Site	No.3 OATS
Test Mode	Mode 5: 800*600/60Hz, D-sub+AV(INPUT)	Product	VGA Card
Test Condition	10m & Vertical	Test Range	30MHz – 1GHz

Frequency MHz	Cable Loss (dB)	Probe Factor (dB/m)	Pre-Amp Factor (dB)	Reading Level (dBuV/m)	Emission Level (dBuV/m)	Margin (dB)	Limit (dBuV/m)
56.420	1.00	5.60	0.00	12.15	18.75	-11.25	30.00
71.870	1.08	6.60	0.00	12.57	20.25	-9.75	30.00
151.910	1.50	9.18	0.00	10.97	21.65	-8.35	30.00
187.870	1.68	8.01	0.00	13.75	23.44	-6.56	30.00
191.883	1.70	8.08	0.00	10.65	20.43	-9.57	30.00
215.981	1.83	9.33	0.00	9.84	21.00	-9.00	30.00
288.012	2.20	12.27	0.00	11.86	26.33	-10.67	37.00

Note:

1. All Reading Levels below 1GHz are Quasi-Peak.
2. Emission Level = Reading Level + Probe Factor + Cable loss – Preamp.
3. Margin = Limit - Emission Level



### 13.3. Test Data of Power Harmonics

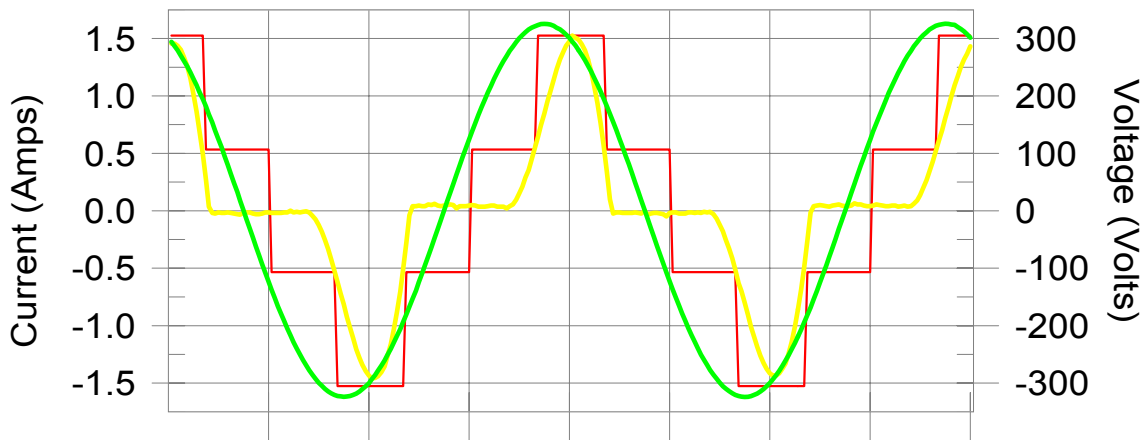
Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 1: 1920*1440/75Hz, D-sub+DVI	Product	VGA Card
Test Condition	Power Harmonics (Classification: Class D)		

Test Result: Pass

Source qualification: Normal

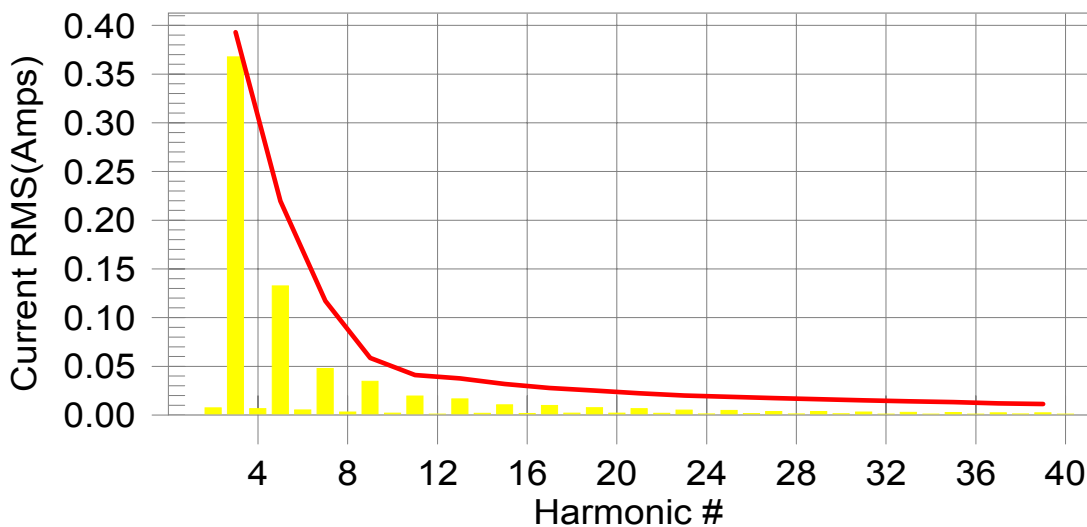
Current & voltage waveforms

It is 0.4% outside Class-D envelope



Harmonics and Class D limit line

European Limits



Test result: Pass

Worst harmonic was #3 with 94.96 % of the limit.

---

 Current Test Result Summary (Run time)

EUT: VGA GARD Tested by: BRIAN  
 Test category: Class D Steady State (European limits) Test Margin: 100  
 Test date: 2002/5/20 Start time: PM 02:38:05 End time: PM 02:40:46  
 Test duration (min): 2.5 Data file name: H-000382.cts\_data  
 Comment: M/N:V8460/DELUXE,V8440/DELUXE MODE:1  
 Customer: ASUS

Test Result: Pass Source qualification: Normal

## Highest parameter values during test:

V_RMS (Volts):	229.71	I_RMS (Amps):	0.661
I_Peak (Amps):	1.525	Crest Factor:	2.364
I_Fund (Amps):	0.564	Power Factor:	0.780
Power (Watts):	118		

Harm#	Harmonics	Limit	% of Limit	Status
2	0.007			
3	0.368	0.393	93.51	Pass
4	0.007			
5	0.133	0.220	60.35	Pass
6	0.005			
7	0.048	0.117	40.76	Pass
8	0.003			
9	0.035	0.059	58.98	Pass
10	0.002			
11	0.019	0.041	47.52	Pass
12	0.001			
13	0.017	0.038	44.01	Pass
14	0.002			
15	0.011	0.032	33.03	Pass
16	0.002			
17	0.010	0.028	35.24	Pass
18	0.002			
19	0.008	0.025	29.97	Pass
20	0.002			
21	0.006	0.022	28.99	Pass
22	0.002			
23	0.005	0.020	25.19	Pass
24	0.001			
25	0.005	0.019	0.00	Pass
26	0.001			
27	0.004	0.017	0.00	Pass
28	0.001			
29	0.004	0.016	0.00	Pass
30	0.001			
31	0.003	0.015	0.00	Pass
32	0.001			
33	0.003	0.014	0.00	Pass
34	0.001			
35	0.003	0.013	0.00	Pass
36	0.001			
37	0.002	0.012	0.00	Pass
38	0.001			
39	0.002	0.011	0.00	Pass
40	0.001			

*Note: Dynamic limits were applied for this test. The highest harmonics values in the above table may not occur at the same window as the maximum harmonics/limit ratio.*

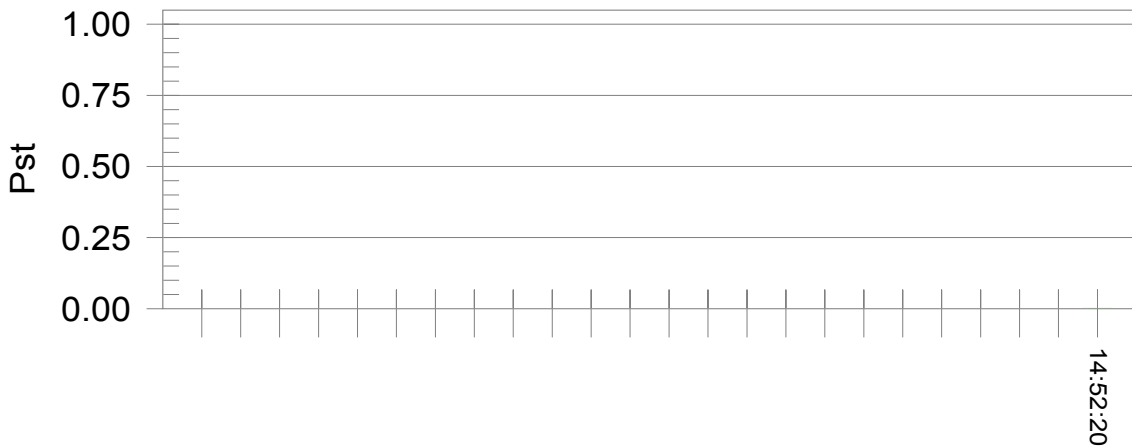
Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 1: 1920*1440/75Hz, D-sub+DVI	Product	VGA Card
Test Condition	Voltage Fluctuations and Flicker		

Test Result: Pass

Status: Test Completed

Pst<sub>i</sub> and limit line

European Limits



Time is too short for Plt plot

Parameter values recorded during the test:

Vrms at the end of test (Volt):	229.79		
Highest dt (%):	0.00	Test limit (%):	4.00
Highest dc (%):	0.00	Test limit (%):	3.30
Highest dmax (%):	0.00	Test limit (%):	4.00
Highest Pst (10 min. period):	0.001	Test limit:	1.000
Highest Plt (2 hr. period):	0.001	Test limit:	0.650

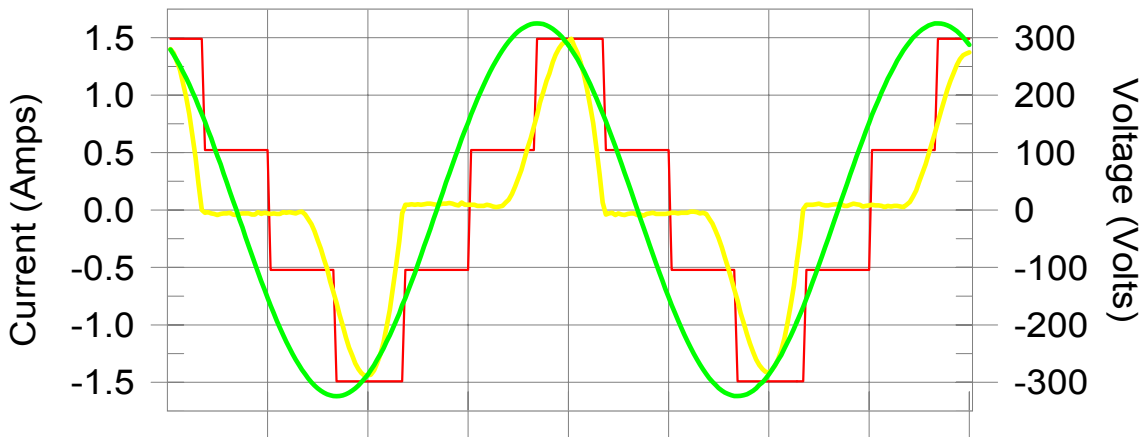
Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 2: 1600*1200/85Hz, D-sub+S-video+AV	Product	VGA Card
Test Condition	Power Harmonics (Classification: Class D)		

Test Result: Pass

Source qualification: Normal

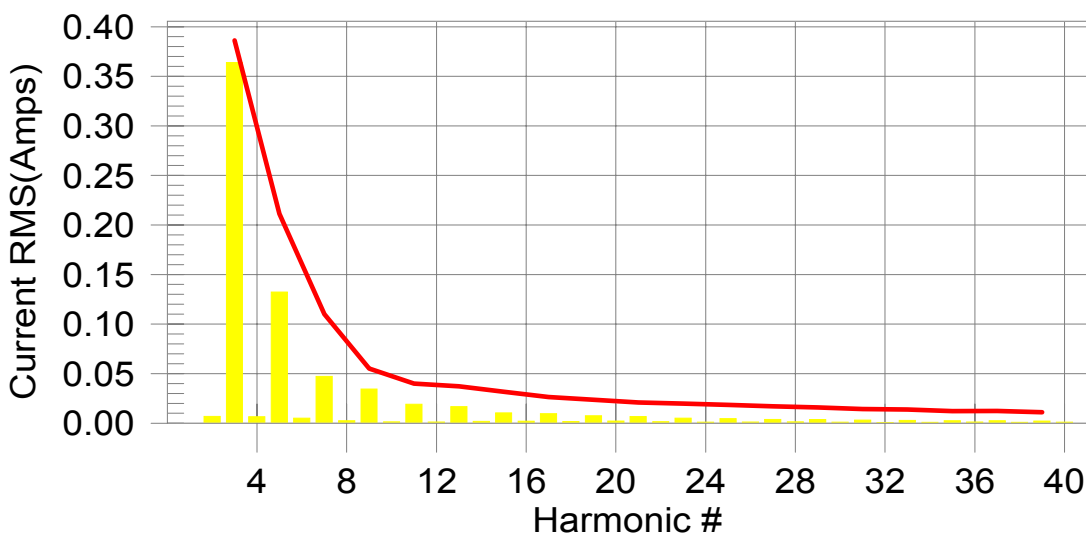
Current & voltage waveforms

It is 3.5% outside Class-D envelope



Harmonics and Class D limit line

European Limits



Test result: Pass

Worst harmonic was #3 with 96.41 % of the limit.

---

 Current Test Result Summary (Run time)

EUT: VGA GARD	Tested by: BRIAN
Test category: Class D Steady State (European limits)	Test Margin: 100
Test date: 2002/5/20	Start time: PM 02:56:45
Test duration (min): 2.5	End time: PM 02:59:26
Comment: M/N:V8460/DELUXE,V8440/DELUXE MODE:2	Data file name: H-000384.cts_data
Customer: ASUS	

Test Result: Pass                      Source qualification: Normal

## Highest parameter values during test:

V_RMS (Volts):	229.66	I_RMS (Amps):	0.649
I_Peak (Amps):	1.491	Crest Factor:	2.400
I_Fund (Amps):	0.556	Power Factor:	0.780
Power (Watts):	116		

Harm#	Harmonics	Limit	% of Limit	Status
2	0.007			
3	0.364	0.386	94.18	Pass
4	0.007			
5	0.132	0.211	62.66	Pass
6	0.005			
7	0.047	0.110	42.89	Pass
8	0.003			
9	0.035	0.055	62.74	Pass
10	0.001			
11	0.019	0.040	47.95	Pass
12	0.001			
13	0.017	0.037	44.68	Pass
14	0.002			
15	0.010	0.032	32.61	Pass
16	0.002			
17	0.010	0.027	36.55	Pass
18	0.002			
19	0.008	0.024	31.83	Pass
20	0.002			
21	0.007	0.021	31.87	Pass
22	0.002			
23	0.005	0.020	26.23	Pass
24	0.001			
25	0.005	0.019	0.00	Pass
26	0.001			
27	0.004	0.017	0.00	Pass
28	0.001			
29	0.004	0.016	0.00	Pass
30	0.001			
31	0.003	0.014	0.00	Pass
32	0.001			
33	0.003	0.014	0.00	Pass
34	0.001			
35	0.002	0.012	0.00	Pass
36	0.001			
37	0.002	0.012	0.00	Pass
38	0.001			
39	0.002	0.011	0.00	Pass
40	0.001			

Note: Dynamic limits were applied for this test. The highest harmonics values in the above table may not occur at the same window as the maximum harmonics/limit ratio.

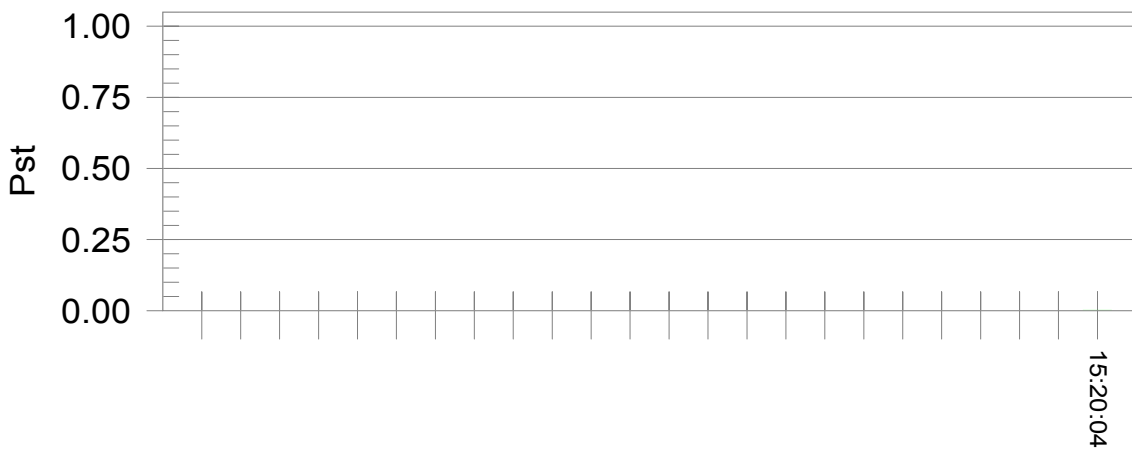
Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 2: 1600*1200/85Hz, D-sub+S-video+AV	Product	VGA Card
Test Condition	Voltage Fluctuations and Flicker		

Test Result: Pass

Status: Test Completed

Pst<sub>i</sub> and limit line

European Limits



Time is too short for Plt plot

Parameter values recorded during the test:

Vrms at the end of test (Volt):	229.80		
Highest dt (%):	0.00	Test limit (%):	4.00
Highest dc (%):	0.00	Test limit (%):	3.30
Highest dmax (%):	0.00	Test limit (%):	4.00
Highest Pst (10 min. period):	0.001	Test limit:	1.000
Highest Plt (2 hr. period):	0.001	Test limit:	0.650



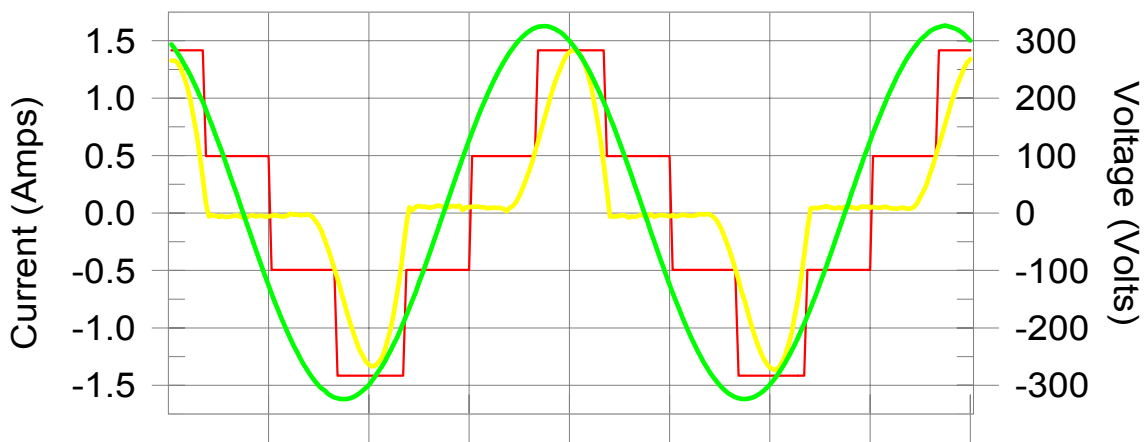
Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 3:1024*768/60Hz, DVI+S-video+AV	Product	VGA Card
Test Condition	Power Harmonics (Classification: Class D)		

Test Result: Pass

Source qualification: Normal

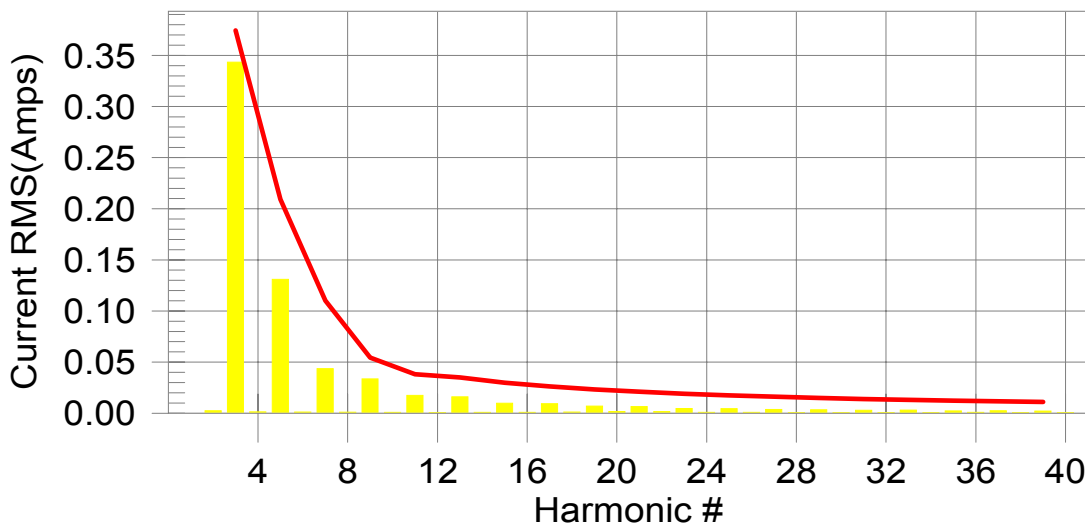
Current & voltage waveforms

It is 0.8% outside Class-D envelope



Harmonics and Class D limit line

European Limits



Test result: Pass

Worst harmonic was #3 with 91.70 % of the limit.

### Current Test Result Summary (Run time)

EUT: VGA GARD	Tested by: BRIAN
Test category: Class D Steady State (European limits)	Test Margin: 100
Test date: 2002/5/20	Start time: PM 02:06:32
Test duration (min): 2.5	End time: PM 02:09:13
Comment: M/N:V8460/DELUXE,V8440/DELUXE MODE:3	Data file name: H-000379.cts_data
Customer: ASUS	

Test Result: Pass Source qualification: Normal

Highest parameter values during test:

V_RMS (Volts): 229.72	I_RMS (Amps): 0.616
I_Peak (Amps): 1.416	Crest Factor: 2.301
I_Fund (Amps): 0.514	Power Factor: 0.778
Power (Watts): 110	

Harm#	Harmonics	Limit	% of Limit	Status
2	0.002			
3	0.343	0.375	91.70	Pass
4	0.001			
5	0.131	0.209	62.55	Pass
6	0.001			
7	0.044	0.110	39.53	Pass
8	0.001			
9	0.034	0.054	61.80	Pass
10	0.001			
11	0.018	0.038	45.97	Pass
12	0.001			
13	0.016	0.035	45.70	Pass
14	0.001			
15	0.010	0.030	32.74	Pass
16	0.001			
17	0.009	0.026	35.25	Pass
18	0.001			
19	0.007	0.023	29.51	Pass
20	0.002			
21	0.007	0.021	31.68	Pass
22	0.002			
23	0.005	0.019	0.00	Pass
24	0.001			
25	0.005	0.018	0.00	Pass
26	0.001			
27	0.004	0.016	0.00	Pass
28	0.001			
29	0.003	0.015	0.00	Pass
30	0.001			
31	0.003	0.014	0.00	Pass
32	0.001			
33	0.003	0.013	0.00	Pass
34	0.001			
35	0.002	0.012	0.00	Pass
36	0.001			
37	0.003	0.012	0.00	Pass
38	0.001			
39	0.002	0.011	0.00	Pass
40	0.001			

*Note: Dynamic limits were applied for this test. The highest harmonics values in the above table may not occur at the same window as the maximum harmonics/limit ratio.*

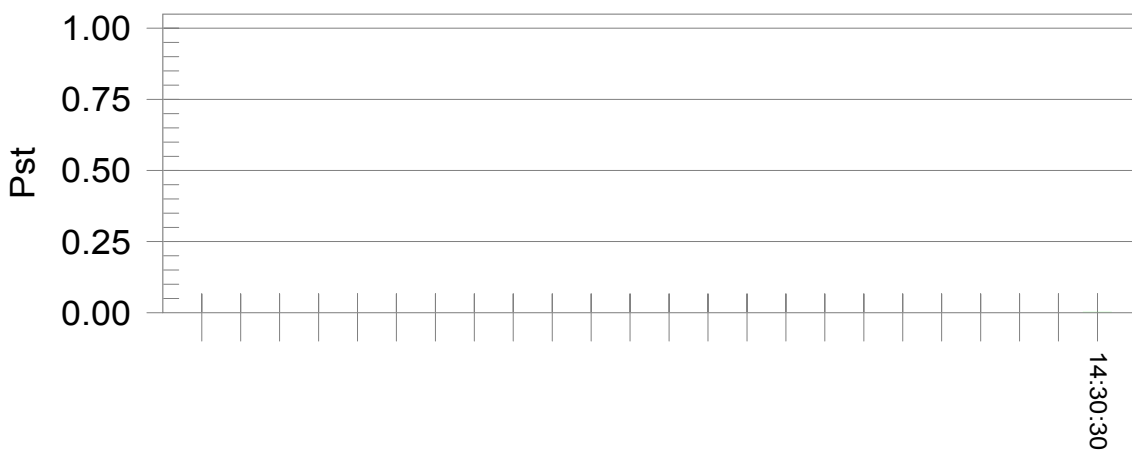
Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 3:1024*768/60Hz, DVI+S-video+AV	Product	VGA Card
Test Condition	Voltage Fluctuations and Flicker		

Test Result: Pass

Status: Test Completed

Pst<sub>i</sub> and limit line

European Limits



Time is too short for Plt plot

Parameter values recorded during the test:

Vrms at the end of test (Volt):	229.90		
Highest dt (%):	0.00	Test limit (%):	4.00
Highest dc (%):	0.00	Test limit (%):	3.30
Highest dmax (%):	0.00	Test limit (%):	4.00
Highest Pst (10 min. period):	0.001	Test limit:	1.000
Highest Plt (2 hr. period):	0.001	Test limit:	0.650

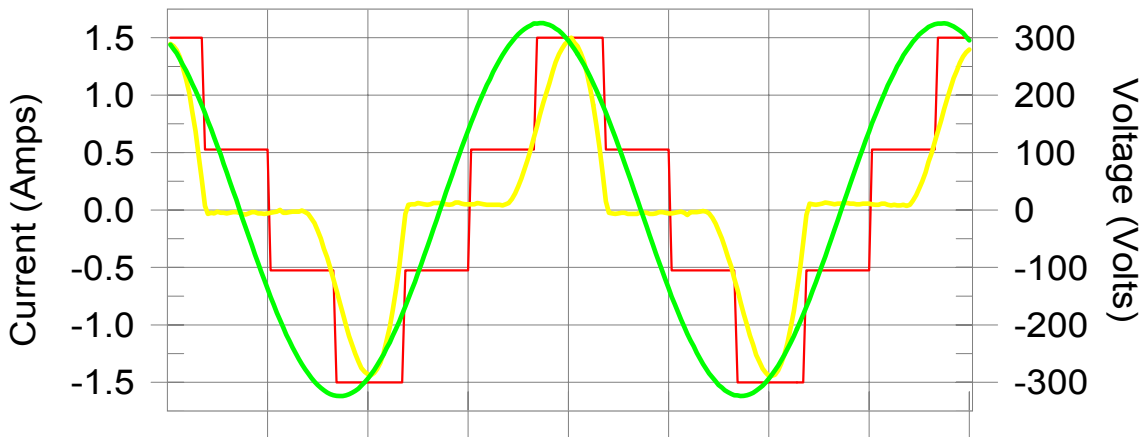
Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 4:1024*768/60Hz, D-sub+S-video(INPUT)	Product	VGA Card
Test Condition	Power Harmonics (Classification: Class D)		

Test Result: Pass

Source qualification: Normal

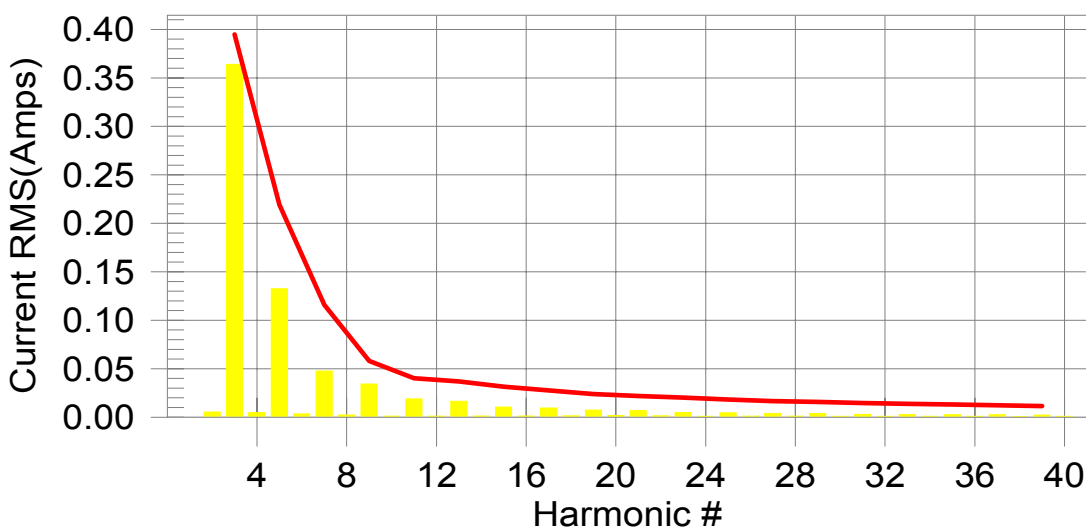
Current & voltage waveforms

It is 2.0% outside Class-D envelope



Harmonics and Class D limit line

European Limits



Test result: Pass

Worst harmonic was #3 with 92.79 % of the limit.

---

 Current Test Result Summary (Run time)

EUT: VGA GARD	Tested by: BRIAN
Test category: Class D Steady State (European limits)	Test Margin: 100
Test date: 2002/5/20	Start time: PM 03:32:34
Test duration (min): 2.5	End time: PM 03:35:14
Comment: M/N:V8460/DELUXE,V8440/DELUXE MODE:4	Data file name: H-000386.cts_data
Customer: ASUS	

Test Result: Pass                      Source qualification: Normal

## Highest parameter values during test:

V_RMS (Volts):	229.66	I_RMS (Amps):	0.651
I_Peak (Amps):	1.501	Crest Factor:	2.331
I_Fund (Amps):	0.553	Power Factor:	0.779
Power (Watts):	116		

Harm#	Harmonics	Limit	% of Limit	Status
2	0.005			
3	0.364	0.395	92.13	Pass
4	0.005			
5	0.132	0.219	60.42	Pass
6	0.003			
7	0.048	0.116	41.17	Pass
8	0.002			
9	0.034	0.058	59.03	Pass
10	0.001			
11	0.019	0.040	47.08	Pass
12	0.001			
13	0.017	0.037	44.80	Pass
14	0.001			
15	0.010	0.031	33.42	Pass
16	0.001			
17	0.010	0.028	34.74	Pass
18	0.001			
19	0.007	0.024	30.32	Pass
20	0.002			
21	0.007	0.022	30.96	Pass
22	0.001			
23	0.005	0.020	0.00	Pass
24	0.001			
25	0.005	0.018	0.00	Pass
26	0.001			
27	0.004	0.017	0.00	Pass
28	0.001			
29	0.004	0.016	0.00	Pass
30	0.001			
31	0.003	0.015	0.00	Pass
32	0.001			
33	0.003	0.014	0.00	Pass
34	0.001			
35	0.003	0.013	0.00	Pass
36	0.001			
37	0.003	0.012	0.00	Pass
38	0.001			
39	0.002	0.012	0.00	Pass
40	0.001			

Note: Dynamic limits were applied for this test. The highest harmonics values in the above table may not occur at the same window as the maximum harmonics/limit ratio.

Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 4:1024*768/60Hz, D-sub+S-video(INPUT)	Product	VGA Card
Test Condition	Voltage Fluctuations and Flicker		

Test Result: Pass

Status: Test Completed

Pst<sub>i</sub> and limit line

European Limits



Time is too short for Plt plot

Parameter values recorded during the test:

Vrms at the end of test (Volt):	229.76		
Highest dt (%):	0.00	Test limit (%):	4.00
Highest dc (%):	0.00	Test limit (%):	3.30
Highest dmax (%):	0.00	Test limit (%):	4.00
Highest Pst (10 min. period):	0.001	Test limit:	1.000
Highest Plt (2 hr. period):	0.001	Test limit:	0.650

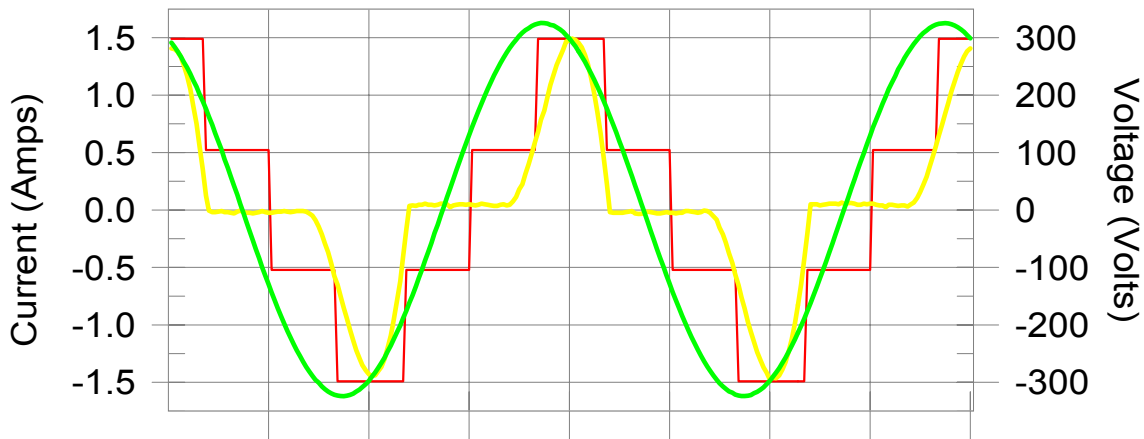
Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 5: 800*600/60Hz, D-sub+AV(INPUT)	Product	VGA Card
Test Condition	Power Harmonics (Classification: Class D)		

Test Result: Pass

Source qualification: Normal

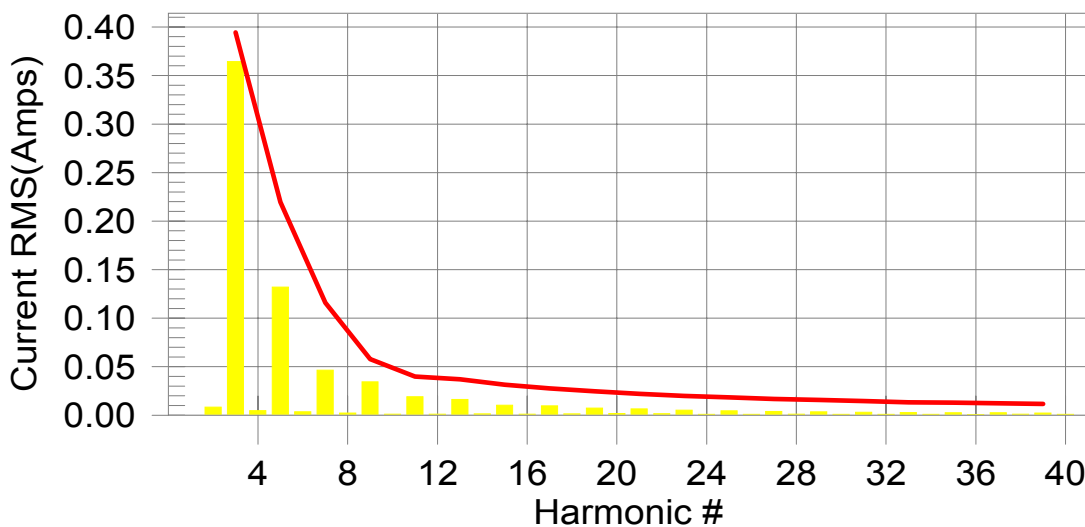
Current & voltage waveforms

It is 1.6% outside Class-D envelope



Harmonics and Class D limit line

European Limits



Test result: Pass

Worst harmonic was #3 with 93.47 % of the limit.

---

 Current Test Result Summary (Run time)

EUT: VGA GARD	Tested by: BRIAN
Test category: Class D Steady State (European limits)	Test Margin: 100
Test date: 2002/5/20	Start time: PM 03:50:32
Test duration (min): 2.5	End time: PM 03:53:13
Comment: M/N:V8460/DELUXE,V8440/DELUXE MODE:5	Data file name: H-000388.cts_data
Customer: ASUS	

Test Result: Pass                      Source qualification: Normal

## Highest parameter values during test:

V_RMS (Volts):	229.66	I_RMS (Amps):	0.653
I_Peak (Amps):	1.491	Crest Factor:	2.336
I_Fund (Amps):	0.558	Power Factor:	0.780
Power (Watts):	117		

Harm#	Harmonics	Limit	% of Limit	Status
2	0.008			
3	0.364	0.394	92.38	Pass
4	0.005			
5	0.132	0.220	60.13	Pass
6	0.004			
7	0.047	0.116	40.14	Pass
8	0.002			
9	0.034	0.058	59.37	Pass
10	0.001			
11	0.019	0.040	47.87	Pass
12	0.001			
13	0.016	0.037	44.03	Pass
14	0.001			
15	0.010	0.032	32.39	Pass
16	0.001			
17	0.010	0.028	34.95	Pass
18	0.002			
19	0.007	0.025	29.67	Pass
20	0.002			
21	0.007	0.022	29.64	Pass
22	0.002			
23	0.005	0.020	25.78	Pass
24	0.001			
25	0.005	0.018	0.00	Pass
26	0.001			
27	0.004	0.017	0.00	Pass
28	0.001			
29	0.004	0.016	0.00	Pass
30	0.001			
31	0.003	0.015	0.00	Pass
32	0.001			
33	0.003	0.013	0.00	Pass
34	0.001			
35	0.003	0.013	0.00	Pass
36	0.001			
37	0.003	0.012	0.00	Pass
38	0.001			
39	0.002	0.012	0.00	Pass
40	0.001			

Note: Dynamic limits were applied for this test. The highest harmonics values in the above table may not occur at the same window as the maximum harmonics/limit ratio.



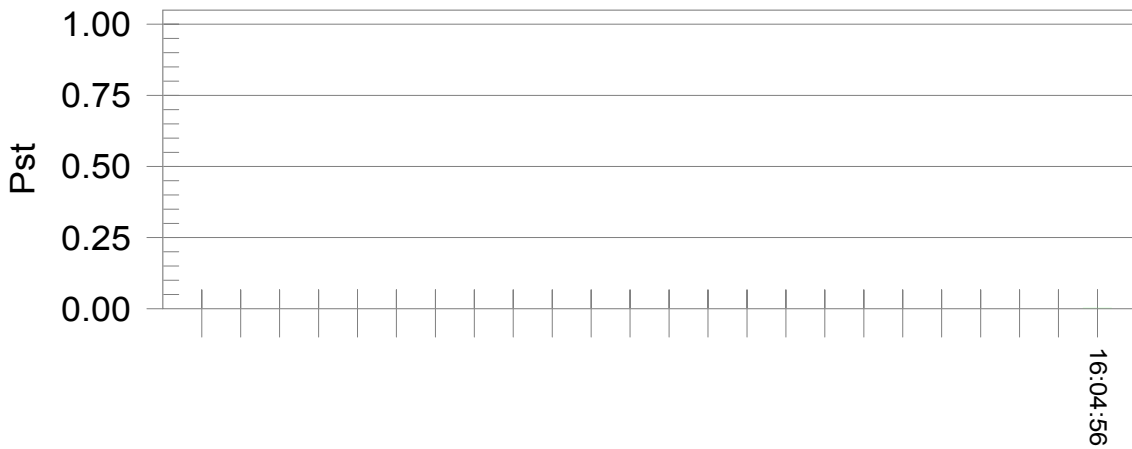
Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 5: 800*600/60Hz, D-sub+AV(INPUT)	Product	VGA Card
Test Condition	Voltage Fluctuations and Flicker		

Test Result: Pass

Status: Test Completed

Pst<sub>i</sub> and limit line

European Limits



Time is too short for Plt plot

Parameter values recorded during the test:

Vrms at the end of test (Volt):	229.64		
Highest dt (%):	0.00	Test limit (%):	4.00
Highest dc (%):	0.00	Test limit (%):	3.30
Highest dmax (%):	0.00	Test limit (%):	4.00
Highest Pst (10 min. period):	0.001	Test limit:	1.000
Highest Plt (2 hr. period):	0.001	Test limit:	0.650

### 13.4. Test Data of Electrostatic Discharge

Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 1: 1920*1440/75Hz, D-sub+DVI	Product	VGA Card
Test Condition	Electrostatic Discharge (Performance Criteria: B)		

Test point	Polarity	Number of Discharges	Complied To Criteria (A,B,C)	Result
Seams	+/-8kV Air	10	A	<b>PASS</b>
Switch	+/-8kV Air	10	A	<b>PASS</b>
Knobs	+/-4kV Con	50	A	<b>PASS</b>
Metal Plate	+/-4kV Con	50	A	<b>PASS</b>
Screws	+/-4kV Con	50	A	<b>PASS</b>
H.C.P.	+/-4kV	50	A	<b>PASS</b>
V.C.P.	+/-4kV	50	A	<b>PASS</b>

#### Criteria judgment of Test result:

- Meet criteria A: No abnormalities were observed during and directly after the test.
- Meet criteria B: Operate as intended after the test
- Meet criteria C: Loss/Error of function
- Additional Information
  - EUT stopped operation and could / could not be reset by operator at \_\_\_\_ kV.
  - No false alarms or other malfunctions were observed during or after the test.

### 13.5. Test Data of Radiated Susceptibility

Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 1: 1920*1440/75Hz, D-sub+DVI	Product	VGA Card
Test Condition	Radiated Susceptibility (Performance Criteria: A)		

Frequency (MHz)	Position (Angle)	Polarity (H or V)	Field Strength (V/m)	Complied To Criteria (A,B,C)	Results
80-1000	0	H	3	A	<b>Pass</b>
80-1000	0	V	3	A	<b>Pass</b>
80-1000	90	H	3	A	<b>Pass</b>
80-1000	90	V	3	A	<b>Pass</b>
80-1000	180	H	3	A	<b>Pass</b>
80-1000	180	V	3	A	<b>Pass</b>
80-1000	270	H	3	A	<b>Pass</b>
80-1000	270	V	3	A	<b>Pass</b>

#### Criteria judgment of Test result:

- Meet criteria A: No abnormalities were observed during and directly after the test.
- Meet criteria B: Operate as intended after the test
- Meet criteria C: Loss/Error of function
- Additional Information
  - EUT stopped operation and could / could not be reset by operator at \_\_\_\_ kV.
  - No false alarms or other malfunctions were observed during or after the test.

**13.6. Test Data of Electrical Fast Transient**

Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 1: 1920*1440/75Hz, D-sub+DVI	Product	VGA Card
Test Condition	Electrical Fast Transient (Performance Criteria: A)		

Inject Line	Polarity	Voltage kV	Inject Time (Second)	Inject Method	Complied To Criteria (A,B,C)	Result
L	±	1kV	60	Direct	A	<b>Pass</b>
N	±	1kV	60	Direct	A	<b>Pass</b>
PE	±	1kV	60	Direct	A	<b>Pass</b>
L+N	±	1kV	60	Direct	A	<b>Pass</b>
L+PE	±	1kV	60	Direct	A	<b>Pass</b>
N+PE	±	1kV	60	Direct	A	<b>Pass</b>
L+N+PE	±	1kV	60	Direct	A	<b>Pass</b>

Criteria judgment of Test result:

- Meet criteria A: No abnormalities were observed during and directly after the test.
- Meet criteria B: Operate as intended after the test
- Meet criteria C: Loss/Error of function
- Additional Information
  - EUT stopped operation and could / could not be reset by operator at \_\_\_\_ kV.
  - No false alarms or other malfunctions were observed during or after the test.

**13.7. Test Data of Surge**

Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 1: 1920*1440/75Hz, D-sub+DVI	Product	VGA Card
Test Condition	Surge (Performance Criteria: A)		

Inject Line	Polarity	Angle	Voltage kV	Time Interval (Second)	Inject Method	Complied To Criteria (A,B,C)	Result
L-N	±	0	1kV	60	Direct	A	<b>Pass</b>
L-N	±	90	1kV	60	Direct	A	<b>Pass</b>
L-N	±	180	1kV	60	Direct	A	<b>Pass</b>
L-N	±	270	1kV	60	Direct	A	<b>Pass</b>
L-PE	±	0	2kV	60	Direct	A	<b>Pass</b>
L-PE	±	90	2kV	60	Direct	A	<b>Pass</b>
L-PE	±	180	2kV	60	Direct	A	<b>Pass</b>
L-PE	±	270	2kV	60	Direct	A	<b>Pass</b>
N-PE	±	0	2kV	60	Direct	A	<b>Pass</b>
N-PE	±	90	2kV	60	Direct	A	<b>Pass</b>
N-PE	±	180	2kV	60	Direct	A	<b>Pass</b>
N-PE	±	270	2kV	60	Direct	A	<b>Pass</b>

**Criteria judgment of Test result:**

- Meet criteria A: No abnormalities were observed during and directly after the test.
- Meet criteria B: Operate as intended after the test
- Meet criteria C: Loss/Error of function
- Additional Information
  - EUT stopped operation and could / could not be reset by operator at \_\_\_\_ kV.
  - No false alarms or other malfunctions were observed during or after the test.

**13.8. Test Data of Conducted Susceptibility**

Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 1: 1920*1440/75Hz, D-sub+DVI	Product	VGA Card
Test Condition	Conducted Susceptibility (Performance Criteria: A)		

Frequency Range (MHz)	Voltage Applied dBuV(V)	Inject Method	Tested Port of EUT	Complied To Criteria (A,B,C)	Result
0.15 ~ 80	130(3V)	CDN 1	AC IN	A	<b>PASS</b>

**Criteria judgment of Test result:**

- Meet criteria A: No abnormalities were observed during and directly after the test.
- Meet criteria B: Operate as intended after the test
- Meet criteria C: Loss/Error of function
- Additional Information
  - EUT stopped operation and could / could not be reset by operator at \_\_\_\_ kV.
  - No false alarms or other malfunctions were observed during or after the test.

**13.9. Test Data of Power Frequency Magnetic Field**

Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 1: 1920*1440/75Hz, D-sub+DVI	Product	VGA Card
Test Condition	Power Frequency Magnetic Field (Performance criteria: A)		

Polarization	Frequency (Hz)	Magnetic Strength (A/m)	Complied To Criteria (A,B,C)	Test Result
X Orientation	50	1	A	<b>PASS</b>
Y Orientation	50	1	A	<b>PASS</b>
Z Orientation	50	1	A	<b>PASS</b>

Criteria judgment of Test result:

- Meet criteria A: No abnormalities were observed during and directly after the test.
- Meet criteria B: Operate as intended after the test
- Meet criteria C: Loss/Error of function
- Additional Information
  - EUT stopped operation and could / could not be reset by operator at \_\_\_\_ kV.
  - No false alarms or other malfunctions were observed during or after the test.

### 13.10. Test Data of Voltage Dips and Interruption

Date of Test	May 22, 2002	Test Room	No.3 Shielded Room
Test Mode	Mode 1: 1920*1440/75Hz, D-sub+DVI	Product	VGA Card
Test Condition	Voltage Dips and Interruption		

Voltage Dips and Interruption Reduction(%)	Angle	Test Duration (Periods)	Required Performance Criteria	Complied To Criteria (A,B,C)	Test Result
>95(0V)	0	0.5	B	A	PASS
>95(0V)	45	0.5	B	A	PASS
>95(0V)	90	0.5	B	A	PASS
>95(0V)	135	0.5	B	A	PASS
>95(0V)	180	0.5	B	A	PASS
>95(0V)	225	0.5	B	A	PASS
>95(0V)	270	0.5	B	A	PASS
>95(0V)	315	0.5	B	A	PASS
30(161V)	0	25	C	A	PASS
30(161V)	45	25	C	A	PASS
30(161V)	90	25	C	A	PASS
30(161V)	135	25	C	A	PASS
30(161V)	180	25	C	A	PASS
30(161V)	225	25	C	A	PASS
30(161V)	270	25	C	A	PASS
30(161V)	315	25	C	A	PASS
>95(0V)	0	250	C	C	PASS
>95(0V)	45	250	C	C	PASS
>95(0V)	90	250	C	C	PASS
>95(0V)	135	250	C	C	PASS
>95(0V)	180	250	C	C	PASS
>95(0V)	225	250	C	C	PASS
>95(0V)	270	250	C	C	PASS
>95(0V)	315	250	C	C	PASS

Criteria judgment of Test result:

- Meet criteria A: No abnormalities were observed during and directly after the test.
- Meet criteria B: Operate as intended after the test
- Meet criteria C: Loss/Error of function
- Additional Information
  - EUT stopped operation and could be reset by operator.
  - No false alarms or other malfunctions were observed during or after the test.



Attachment 1 : EUT Test Photographs

### Attachment 1 : EUT Test Photographs

Front View of Conducted Test (Mode 1)



Back View of Conducted Test (Mode 1)



Front View of Conducted Test (Mode 2)



Back View of Conducted Test (Mode 2)



Front View of Conducted Test (Mode 3 )



Back View of Conducted Test (Mode 3)





Front View of Conducted Test (Mode 4 )



Back View of Conducted Test (Mode 4)



Front View of Conducted Test (Mode 5 )



Back View of Conducted Test (Mode 5)

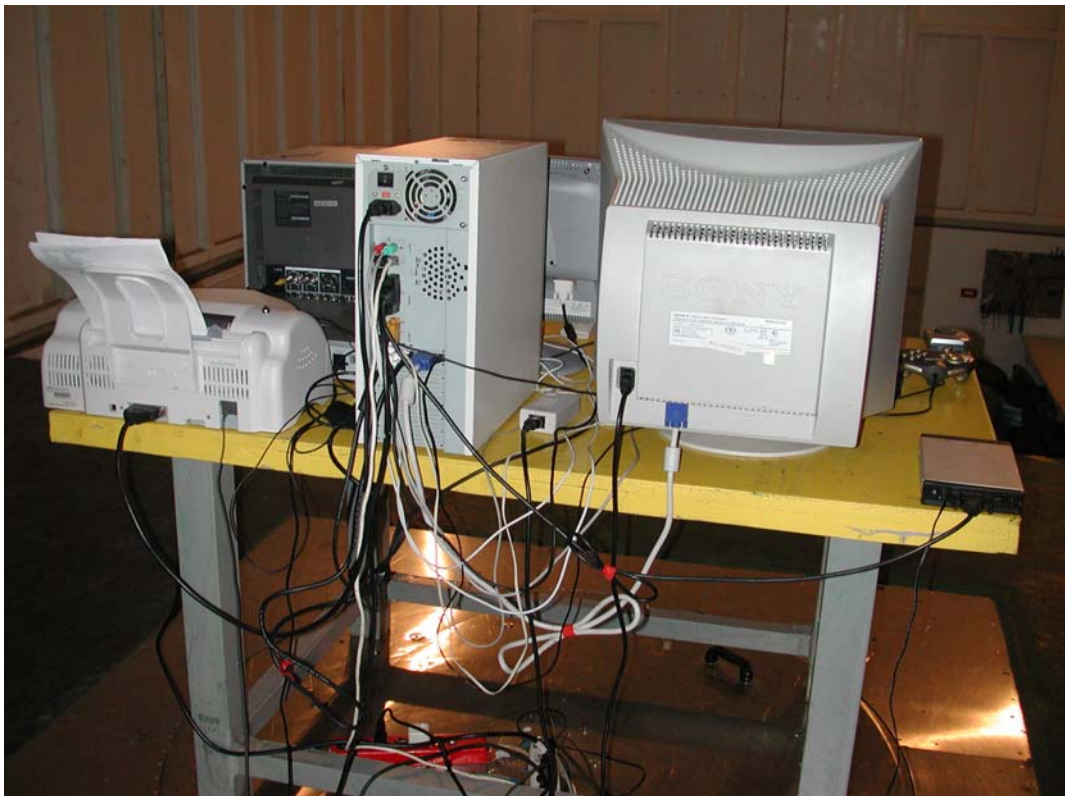




Front View of Radiated Test (Mode 1)



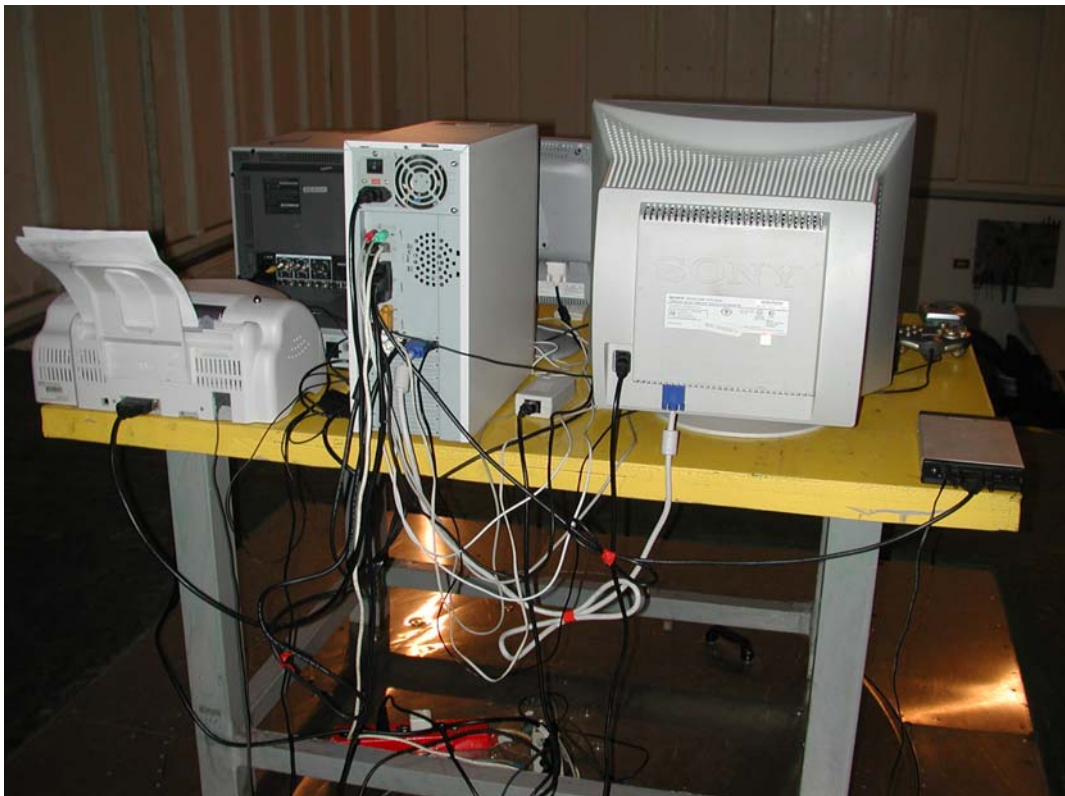
Back View of Radiated Test (Mode 1)



Front View of Radiated Test (Mode 2)



Back View of Radiated Test (Mode 2)

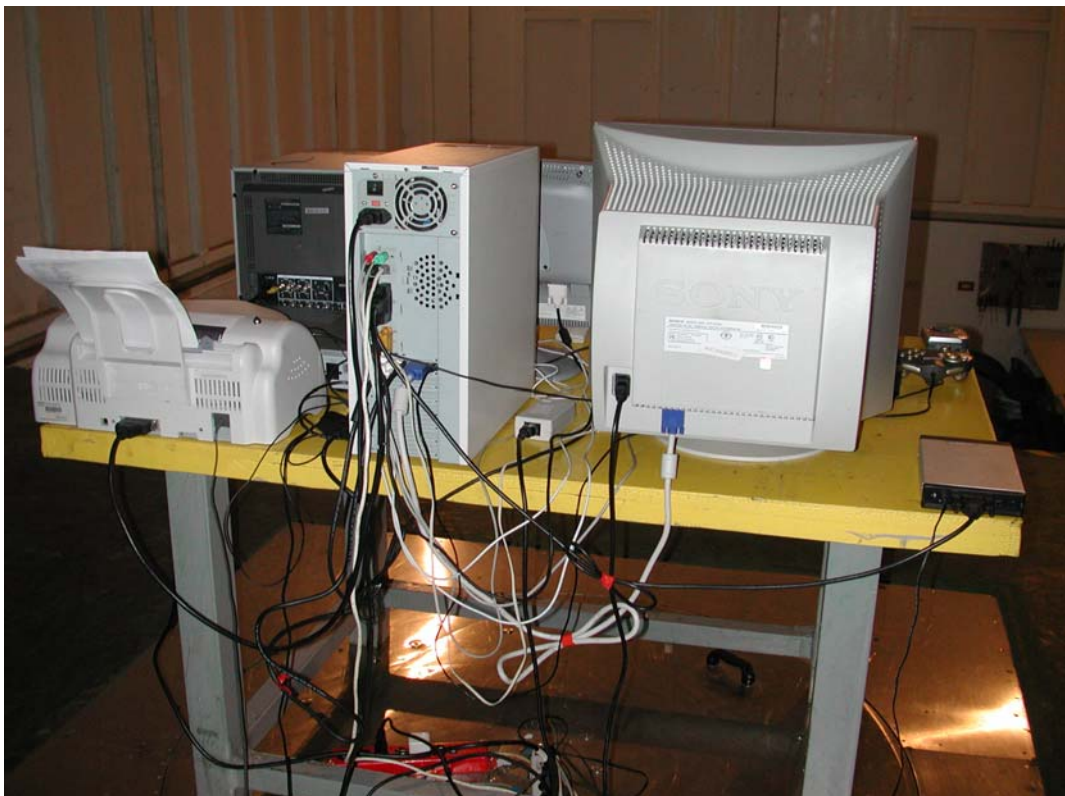




Front View of Radiated Test (Mode 3)



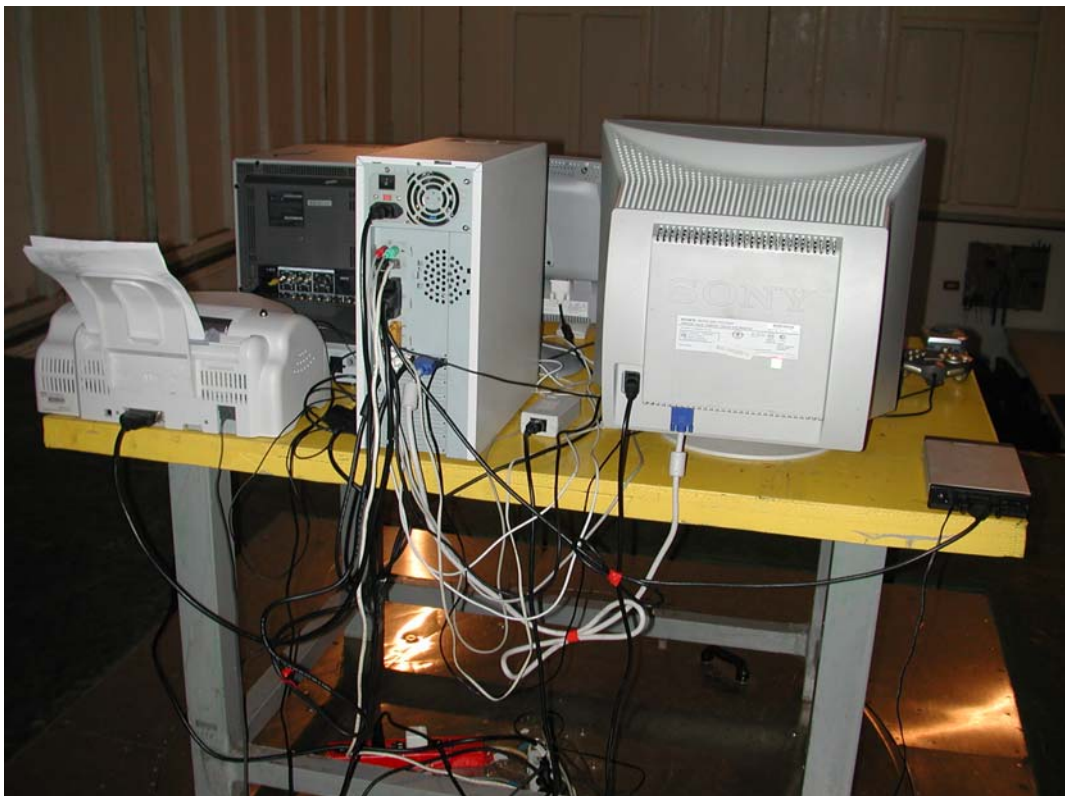
Back View of Radiated Test (Mode 3)



Front View of Radiated Test (Mode 4)



Back View of Radiated Test (Mode 4)





Front View of Radiated Test (Mode 5)



Back View of Radiated Test (Mode 5)



Harmonics Test Setup (Mode 1)



Harmonics Test Setup (Mode 2)



Harmonics Test Setup (Mode 3)



Harmonics Test Setup (Mode 4)





Harmonics Test Setup (Mode 5)



ESD Test Setup



RS Test Setup



EFT/B Test Setup



Surge Test Setup



CS Test Setup





Power Frequency Magnetic Field Test Setup



Dips Test Setup



## Attachment 2 : EUT Detailed Photographs

## Attachment 2 : EUT Detailed Photographs

(1) EUT Photo



(2) EUT Photo





(3) EUT Photo



(4) EUT Photo (V8440 /Deluxe)





(5) EUT Photo (V8440/Deluxe)



(6) EUT Photo (V8440/TDV)





(7) EUT Photo (V8440/TDV)



(8) EUT Photo(V8460 Deluxe)





(9) EUT Photo(V8460 Deluxe)



(10) EUT Photo(V8460/TDV)



(11) EUT Photo(V8460/TDV)





**Reference : Laboratory of License**

## Scope of Accreditation



### ELECTROMAGNETIC COMPATIBILITY AND TELECOMMUNICATIONS

NVLAP LAB CODE 200533-0

#### QUITEK CORPORATION

No. 5, Ruei-shu Valley, Ruei-ping, Tsuen  
Lin Kou Shiang, Taipei 244  
TAIWAN

Mr. Gene Chang

Phone: 886-2-8601-3788 Fax: 886-2-8601-3789

E-Mail: gene@quietek.com

#### *NVLAP Code Designation / Description*

#### **Emissions Test Methods:**

12/CIS22	IEC/CISPR 22 (1997) and EN 55022 (1998): Limits and methods of measurement of radio disturbance characteristics of information technology equipment
12/CIS22a	IEC/CISPR 22:1993: Limits and methods of measurement of radio disturbance characteristics of information technology equipment, Amendment 1:1995, and Amendment 2:1996.
12/CIS22b	CNS 13438:1997: Limits and Methods of Measurement of Radio Interference Characteristics of Information Technology Equipment
12/F01	FCC Method - 47 CFR Part 15 - Digital Devices
12/F01a	Conducted Emissions, Power Lines, 450 KHz to 30 MHz
12/F01b	Radiated Emissions

June 30, 2003

*Effective through*

*For the National Institute of Standards and Technology*

## Scope of Accreditation



### ELECTROMAGNETIC COMPATIBILITY AND TELECOMMUNICATIONS

NVLAP LAB CODE 200533-0

#### QUITEK CORPORATION

*NVLAP Code*    *Designation / Description*

12/T51            AS/NZS 3548: Electromagnetic Interference - Limits and Methods of Measurement  
of Information Technology Equipment

#### Immunity Test Methods:

- 12/I01            IEC 61000-4-2 (1995) and Amendment 1 (1998): Electrostatic Discharge Immunity  
Test
- 12/I02            IEC 61000-4-3 (1995) and Amendment 1 (1998): Radiated, Radio-Frequency  
Electromagnetic Field Immunity Test
- 12/I03            IEC 61000-4-4 (1995): Electrical Fast Transient/Burst Immunity Test
- 12/I04            IEC 61000-4-5 (1995): Surge Immunity Test
- 12/I05            IEC 61000-4-6 (1996): Immunity to Conducted Disturbances, Induced  
Radio-Frequency Fields
- 12/I06            IEC 61000-4-8 (1993): Power Frequency Magnetic Field Immunity Test
- 12/I07            IEC 61000-4-11 (1994): Voltage Dips, Short Interruptions and Voltage Variations  
Immunity Tests

June 30, 2003

*David F. Alderman*

*Effective through*

*For the National Institute of Standards and Technology*



United States Department of Commerce  
National Institute of Standards and Technology

**NVLAP**<sup>®</sup>

ISO/IEC GUIDE 25:1990  
ISO 9002:1987

Certificate of Accreditation



**QUITEK CORPORATION**

LIN KOU SHIANG, TAIPEI 244  
TAIWAN

*is recognized under the National Voluntary Laboratory Accreditation Program for satisfactory compliance with criteria established in Title 15, Part 285 Code of Federal Regulations. These criteria encompass the requirements of ISO/IEC Guide 25 and the relevant requirements of ISO 9002 (ANSI/ASQC Q92-1987) as suppliers of calibration or test results. Accreditation is awarded for specific services, listed on the Scope of Accreditation for:*

**ELECTROMAGNETIC COMPATIBILITY AND TELECOMMUNICATIONS**

June 30, 2003

Effective through

*David F. Alderman*

For the National Institute of Standards and Technology

NVLAP Lab Code: 200533-0

## **EMC Laboratory Authorisation**

**Aut. No. : ELA 165**

**EMC Laboratory:**

**Quietek Corporation  
No. 75-2, Wang-Yeh Valley,  
Yung-Hsing, Chiung-Lin, Hsin-Chu,  
Hsin-Chu County, Taiwan R.O.C.**

**Scope of Authorization: All CENELEC standards [ENs] for EMC that are listed on the accompanying page, and, all of the corresponding CISPR, IEC, and ISO EMC standards that are listed on the accompanying page.**

This Authorisation Document confirms that the above mentioned EMC Laboratory has been validated against EN 45001 and found to be compliant. The laboratory also fulfils the conditions described in Nemko Document ELA 10. During Nemko's visit to the laboratory, an assessment was made of the relevant parts of your organisation - i.e. facilities, personnel qualifications, test equipment, and testing practices. It was found that the EMC Laboratory is capable of performing tests within the Scope of Authorisation given on the accompanying page. Accordingly, Nemko will accept your test reports as a basis for attesting conformity to these EMC Standards for the products in question under the European Union EMC Directive [89/336/EEC as amended by 92/31/EEC and 98/13/EC].

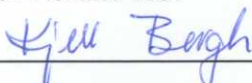
In case of applications for Product Certification(s) to be issued by Nemko, your EMC Laboratory's test report(s) will be accepted by Nemko if they are enclosed with the Application Form submitted by the manufacturer.

In order to maintain this Authorization, the information given in the enclosed ELA-INFOs (if any) must be carefully followed. Nemko is to be promptly notified about any changes in the situation at your EMC Laboratory which may affect the basis for this Authorization. The Authorization may at any time be withdrawn if the conditions are no longer considered to be fulfilled.

The Authorisation is valid through **31. December 2003**.

**Oslo, 18. April 2001**

For Nemko AS:



Kjell Bergh, Nemko Group EMC Co-ordinator



## **EMC Laboratory Authorisation**

**Aut. No. : ELA 162**

EMC Laboratory:

**Quietek Corporation  
No. 75-2, Wang-Yeh Valley,  
Yung-Hsing, Chiung-Lin, Hsin-Chu,  
Hsin-Chu County, Taiwan R.O.C.**

Scope of Authorization:

**EN 60601-1-2 and IEC 60601-1-2, the Collateral Standards  
for electromedical products, with particular application to  
EMC requirements only.**

This Authorisation Document confirms that the above mentioned EMC Laboratory has been validated against EN 45001 and found to be compliant. The laboratory also fulfils the conditions described in Nemko Document ELA 10. During Nemko's visit to the laboratory, an assessment was made of the relevant parts of your organisation - i.e. facilities, personnel qualifications, test equipment, and testing practices. It was found that the EMC Laboratory is capable of performing tests within the Scope of Authorisation listed above. Accordingly, Nemko will accept your test reports as a basis for attesting conformity to these EMC Standards for the products in question under either the European Union Medical Device Directive [MDD], 93/42/EEC, or the European Union Active Implantable Medical Device Directive [AIMD], 90/385/EEC, (as applicable).

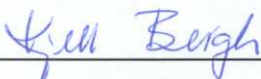
In case of applications for Product Certification(s) to be issued by Nemko, your EMC Laboratory's test report(s) will be accepted by Nemko if they are enclosed with the Application Form submitted by the manufacturer.

In order to maintain the Authorisation, the information given in the enclosed ELA-INFOs (if any) must be carefully followed. Nemko is to be promptly notified about any changes in the situation at your EMC Laboratory which may affect the basis for this Authorisation. The Authorisation may at any time be withdrawn if the conditions are no longer considered to be fulfilled.

The Authorisation is valid through **31. December 2001**.

**Oslo, 18. April 2001**

For Nemko AS:



Kjell Bergh, Nemko Group EMC Co-ordinator

## EMC Laboratory Authorisation

Aut. No. : ELA 191

(Page 2 of 2)

### SCOPE OF AUTHORISATION

#### Generic and product-family standards – R&TTE Directive

EN 300 220-3 :2000	ETS 300 328:1996 + A1:97 EN 300 328-2:2000	I-ETS 300 330:1994 + A1:97 (Not harmonised for R&TTE-D)
EN 300 422-2 :2000	I-ETS 300 440:1995 (Not harmonised for R&TTE-D)	ETS 300 445 :1996 + A1 :97 EN 301 489-09 :2000
ETS 300 683 :1997 EN 301 489-03 :2000	ETS 300 826 :1997 EN 301 489-17 :2000	EN 301 489-01:2000

#### Basic standards

EN 61000-4-2:1995 + A1:98 IEC 61000-4-2:1995 + A1:98  (EN 60801-1:1993 IEC 801.2:1991 IEC 801.2:1984)	EN 61000-4-3:1996 + A1:98 IEC 61000-4-3:1995 + A1:98  (IEC 801.3:1984 ENV 50140:1993 + ENV 50204:1995)	EN 61000-4-4:1995 IEC 61000-4-4:1995  (IEC 801.4:1990)
EN 61000-4-5:1995 IEC 61000-4-5:1995  (ENV 50142:1994)	EN 61000-4-6:1996 IEC 61000-4-6:1996  (ENV 50141:1993)	EN 61000-4-8:1993 IEC 61000-4-8:1993
EN 61000-4-11:1994 IEC 61000-4-11:1994		

Oslo, 24 April 2001

Kjell Bergh, Nemko Group EMC Co-ordinator



**EMC Laboratory  
Authorisation****Aut. No. : ELA 162**

EMC Laboratory: **QuieTek Corporation  
No. 75-2, Wang-Yeh Valley,  
Yung-Hsing, Chiung-Lin, Hsin-Chu,  
Hsin-Chu County, Taiwan R.O.C.**

Scope of Authorization: **EN 60601-1-2 and IEC 60601-1-2, the Collateral Standards  
for electromedical products, with particular application to  
EMC requirements only.**

This Authorisation Document confirms that the above mentioned EMC Laboratory has been validated against EN 45001 and found to be compliant. The laboratory also fulfils the conditions described in Nemko Document ELA 10. During Nemko's visit to the laboratory, an assessment was made of the relevant parts of your organisation - i.e. facilities, personnel qualifications, test equipment, and testing practices. It was found that the EMC Laboratory is capable of performing tests within the Scope of Authorisation listed above. Accordingly, Nemko will accept your test reports as a basis for attesting conformity to these EMC Standards for the products in question under either the European Union Medical Device Directive [MDD], 93/42/EEC, or the European Union Active Implantable Medical Device Directive [AIMD], 90/385/EEC, (as applicable).

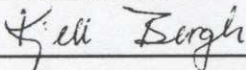
In case of applications for Product Certification(s) to be issued by Nemko, your EMC Laboratory's test report(s) will be accepted by Nemko if they are enclosed with the Application Form submitted by the manufacturer.

In order to maintain the Authorisation, the information given in the enclosed ELA-INFOs (if any) must be carefully followed. Nemko is to be promptly notified about any changes in the situation at your EMC Laboratory which may affect the basis for this Authorisation. The Authorisation may at any time be withdrawn if the conditions are no longer considered to be fulfilled.

The Authorisation is valid through **31. December 2003.**

**Oslo, 24. April 2001**

For Nemko AS:

**Kjell Bergh, Nemko Group EMC Co-ordinator**



**EMC Laboratory Authorisation**
**Aut. No. : ELA 165**
**(Page 2 of 2)**
**SCOPE OF AUTHORIZATION**
**GENERIC & PRODUCT-FAMILY STANDARDS**

EN 50081-1:1992 IEC 61000-6-3  EN 50081-2:1993 IEC 61000-6-4:1997	EN 50082-1:1992 EN 50082-1 :1997 IEC 61000-6-1:1997  EN 50082-2:1995 EN 61000-6-2:1999 IEC 61000-6-2:1999	EN 50091-2:1995
EN 50130-4:1995 + A1:98	EN 55011:1991 + A1:97 + A2:96 CISPR 11:1990 + A1:96 + A2:96 EN 55011:1998 + CISPR 11:97	EN 55013:90 + A12:94 + A13:96 + A14 :99 CISPR 13:75 + A1:83
EN 55014-1:1993 + A1:97 + A2 :99 CISPR 14:1993 + A1:96 + A2 :	EN 55014-2:1997 CISPR 14-2:1997 EN 55104:1995	EN 55015:1993, CISPR 15:1992 EN 55015:1996 + A1:97 CISPR 15:96 + A1:97
EN 55022:1994 + A1:95 + A2:97 CISPR 22:1993 + A1:95 + A2:96 EN 55022:1998, CISPR 22:1997	EN 55024:1998 CISPR 24:1997	EN 55103-1:1996
EN 55103-2:1996		
EN 61000-3-2:1995 + A1:98 + A2:98 + A14 :00 IEC 61000-3-2:1995 + A1:97 + A2:98 IEC 61000-3-2 :2000	EN 61000-3-3:1995, IEC 61000-3-3:1994 EN 61000-3-11 :2000 IEC 61000-3-11 :2000	EN 61326-1:1997 + A1:98 IEC 61326:1997 + A1:98

**BASIC STANDARDS**

EN 61000-4-2:1995 + A1:98 IEC 61000-4-2:1995 + A1:98  (EN 60801-1:1993 IEC 801.2:1991 IEC 801.2:1984)	EN 61000-4-3:1996 + A1:98 IEC 61000-4-3:1995 + A1:98  (IEC 801.3:1984 ENV 50140:1993 + ENV 50204:1995)	EN 61000-4-4:1995 IEC 61000-4-4:1995  (IEC 801.4:1990)
EN 61000-4-5:1995 IEC 61000-4-5:1995  (ENV 50142:1994)	EN 61000-4-6:1996 IEC 61000-4-6:1996  (ENV 50141:1993)	EN 61000-4-8:1993 IEC 61000-4-8:1993
EN 61000-4-11:1994 IEC 61000-4-11:1994		

**Oslo, 24 April 2001**
**Kjell Bergh, Nemko Group EMC Co-ordinator**
*Postal address:*
*Telephone:* +47 22 96 03 30

P.O.Box 73 Blindern

*Fax:* +47 22 96 05 50

N-0314 OSLO, NORWAY



**EMC Laboratory Authorisation****Aut. No. : ELA 191****Testing of  
Radio & Telecommunications Terminal Equipment**

**EMC  
Laboratory:** **QuieTek Corporation**  
**No. 75-2, Wang-Yeh Valley,**  
**Yung-Hsing, Chiung-Lin, Hsin-Chu,**  
**Hsin-Chu County, Taiwan R.O.C.**

**Scope of  
Authorisation:** **All CENELEC and ETSI standards [ENs and ETSs that are listed on the  
accompanying page, and, all of the corresponding CISPR, IEC, and ISO EMC  
standards]. This authorisation covers all of the EMC-related testing and  
documentation within the scope of the *Radio and Telecommunications Terminal  
Equipment [R&TTE] Directive [i.e. 1999/5/EC].***

**NOTE: This authorisation also covers EMC-related testing and documentation  
that is within the scope of Article 10.5 of the *EMC Directive [i.e. 89/336/EEC as  
amended by 92/31/EEC]***

This Authorisation Document confirms that the above mentioned EMC Laboratory has been validated against EN 45001 and found to be compliant. The laboratory also fulfils the conditions described in Nemko Document ELA 10. During Nemko's visit to the laboratory, an assessment was made of the relevant parts of your organisation - i.e. facilities, personnel qualifications, test equipment, and testing practices. It was found that the EMC Laboratory is capable of performing tests within the Scope of Authorisation given on the accompanying page. Accordingly, Nemko will accept your test reports as a basis for attesting conformity to these EMC Standards for the products in question under the European Union's Directives specified above

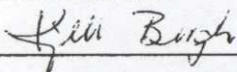
For Type Examination Certification(s) to be issued by Nemko, your EMC Laboratory's test report(s) will be accepted by Nemko if they are enclosed with the Application Form submitted by the manufacturer.

In order to maintain the Authorisation, the information given in the enclosed ELA-INFOs (if any) must be carefully followed. Nemko is to be promptly notified about any changes in the situation at your EMC Laboratory which may affect the basis for this Authorisation. The Authorisation may at any time be withdrawn if the conditions are no longer considered to be fulfilled.

The Authorisation is valid through **31. December 2003.**

**Oslo, 24. April 2001**

For Nemko AS:

  
\_\_\_\_\_

Kjell Bergh, Nemko Group EMC Co-ordinator