

## Qualified Vendors List (QVL) Standard table for user manual

Project Name:M2R32-MVP

Size	Vendor	Chip No.	CL	Chip Brand	SS/DS	Part No.	DIMM socket support (Optional)		
							A*	B*	C*
DDR2 533 256MB	KINGSTON	E5116AF-5C-E	N/A	N/A	SS	KVR533D2N4/256	√	√	√
DDR2 533 512MB	KINGSTON	HYB18T512800AF37	N/A	N/A	SS	KVR533D2N4/512	√	√	√
DDR2 533 1024MB	KINGSTON	5YDIID9GCT	N/A	N/A	DS	KVR533D2N4/1G	√	√	
DDR2 533 256MB	Qimonda	HYB18T512160AF-3.7	N/A	N/A	SS	HYS64T32000HU-3.7-A	√	√	√
DDR2 533 512MB	Qimonda	HYB18T512800AF37	N/A	N/A	SS	HYS64T64000HU-3.7-A	√	√	√
DDR2 533 1024MB	Qimonda	HYB18T512800AF37	N/A	N/A	DS	HYS64T128020HU-3.7-A	√	√	
DDR2 533 256MB	Qimonda	HYB18T5121608BF-3.7	N/A	N/A	SS	HYS64T32000HU-3.7-B	√	√	√
DDR2 533 512MB	Qimonda	HYB18T512800BF37	N/A	N/A	SS	HYS64T64000HU-3.7-B	√	√	√
DDR2 533 1024MB	Qimonda	HYB18T512800BF37	N/A	N/A	DS	HYS64T128020HU-3.7-B	√	√	
DDR2 533 256MB	Qimonda	HYB18T256800AF37(ECC)	N/A	N/A	SS	HYS72T32000HU-3.7-A	√	√	√
DDR2 533 1024MB	Qimonda	HYB18T512800AF37(ECC)	N/A	N/A	DS	HYS72T128020HU-3.7-A	√	√	
DDR2 533 512MB	Hynix	HY5PS12821F-C4	N/A	N/A	SS	HYMP564U648-C4	√	√	√
DDR2 533 1024MB	Hynix	HY5PS12821F-C4	N/A	N/A	DS	HYMP512U648-C4	√	√	
DDR2 533 512MB	Hynix	HY5PS12821FP-C4(ECC)	N/A	N/A	SS	HYMP564U728-C4	√	√	√
DDR2 533 512MB	Hynix	HY5PS12821AFP-C3	N/A	N/A	SS	HYMP564U64AP8-C3	√	√	√
DDR2 533 1024MB	Hynix	HY5PS12821AFP-C3	N/A	N/A	DS	HYMP512U64AP8-C3	√	√	
DDR2 533 512MB	ELPIDA	E5108AB-5C-E	N/A	N/A	SS	EBE51UD8ABFA-5C-E	√	√	√
DDR2 533 256MB	Apacer	E5116AB-5C-E	N/A	N/A	SS	78.81077.420	√	√	√
DDR2 533 512MB	KINGMAX	E5108AE-5C-E	N/A	N/A	SS	KLBC28F-A8EB4	√	√	√
DDR2 533 1024MB	KINGMAX	E5108AE-5C-E	N/A	N/A	DS	KLBD48F-A8EB4	√	√	
DDR2 533 512MB	KINGMAX	KKEA88E4AAK-37	N/A	N/A	SS	KLBC28F-A8KE4	√	√	√
DDR2 533 1024MB	KINGMAX	5MB22D9DCN	N/A	N/A	DS	KLBD48F-A8ME4	√	√	

Note:

A\* : Supports one module inserted in any slot as Single-channel memory configuration

B\* : Supports one pair of modules inserted into either the yellow slots or the black slots as one pair of Dual-channel memory configuration

C\* : Supports 4 modules inserted into both the yellow and black slots as two pairs of Dual-channel memory configuration