

Appendix D Sync. Clock Period & Sync. Clock Frequency

Changes to the SCSI Synchronous Clock Period of each SCSI target is a low-level control of the SCSI controller chip. There is no other selectable option. Only the Synchronous Clock Period is available in this field.

To calculate the Synchronous Clock Period, refer to the “Sample equation” to get the correct Synchronous Clock Period.

Sample equation:

$$\frac{1}{20.8\text{Mhz} \times 4\text{ns}} = \frac{1}{20.8 \times 10^6 \times 4 \times 10^{-9}} = 12$$

$$\frac{1}{12 \times 4\text{ns}} = \frac{1}{12 \times 4 \times 10^{-9}} = 20.8\text{Mhz}$$

In this example, where “20.8Mhz” is called the Synchronous Clock Frequency, and “12” is called the Synchronous Clock Period.

Synchronous Clock Period	Synchronous Clock Frequency	Synchronous Clock Period	Synchronous Clock Frequency
6	41.6 Mhz	50	5.0 Mhz
8	31.2 Mhz	62	4.0 Mhz
10	25.0 Mhz	75	3.3 Mhz
12	20.8 Mhz	88	2.8 Mhz
15	16.6 Mhz	100	2.5 Mhz
18	13.8 Mhz	110	2.2 Mhz
25	10.0 Mhz	120	2.0 Mhz
31	8.0 Mhz	135	1.8 Mhz
37	6.7 Mhz	0	Asynchronous
43	5.8 Mhz		

