Settings for ASUS AP2500/AP7500 Server (P2B-D2)

IMPORTANT: The User's Manual contains important notes. You may use this diagram for reference purposes only after reading the User's Manual completely.

960SEL

Bridge (Def)

I_2O

I960 Setting

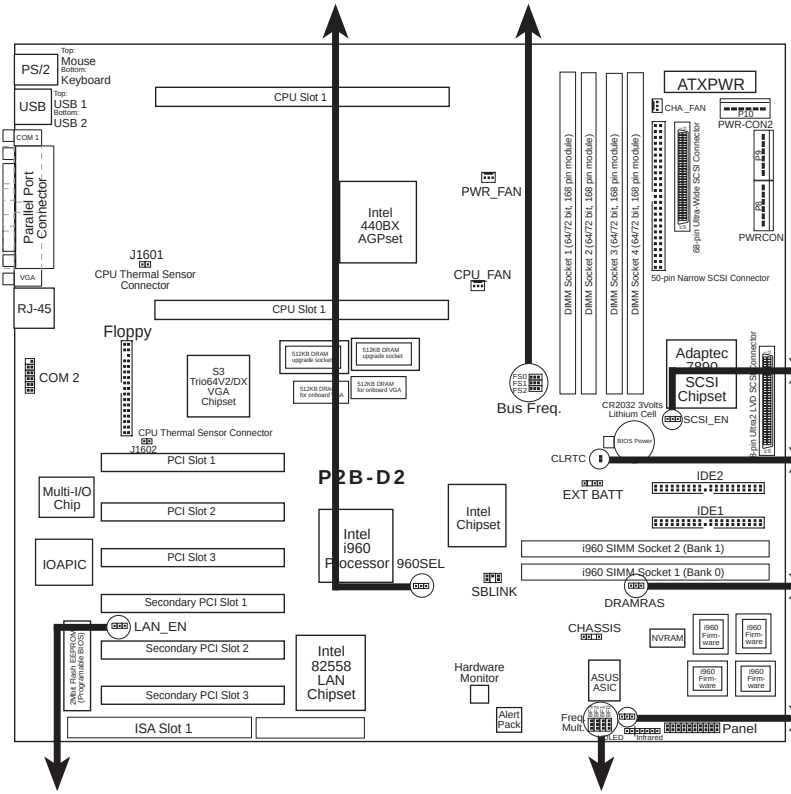
FS0 FS1 FS2

50MHz 66MHz 75MHz 83MHz 100MHz

CPU Bus Frequency

Intel Pentium III/II

550MHz = 5.5x 100MHz
500MHz = 5.0x 100MHz
450MHz = 4.5x 100MHz
400MHz = 4.0x 100MHz
350MHz = 3.5x 100MHz
333MHz = 5.0x 66MHz
300MHz = 4.5x 66MHz
266MHz = 4.0x 66MHz
233MHz = 3.5x 66MHz



SCSI_EN

Enable (Def)

Disable

Onboard SCSI

CLRTC

Short Solder Points to Clear RTC RAM

Real Time Clk RAM

DRAMRAS

Single-Sided DRAMs (Def)

Double-Sided DRAMs

DRAM RAS Setting

JP1601

Disable (Def)

Enable

Chassis Intrusion

LAN_EN

Enable (Def)

Disable

Onboard LAN

BF3 BF2 BF1 BF0

2.0x (2/1) 2.5x (5/2) 3.0x (3/1) 3.5x (7/2) 4.0x (4/1) 4.5x (9/2) 5.0x (5/1) 5.5x (11/2) 6.0x (6/1)

CPU Core:Bus Frequency Multiple